

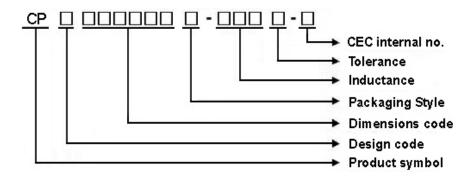
#### ISO9001 & ISO14001 & TS16949 CHILISIN ELECTRONICS CORP.

### Lead-Free & RoHs Compliance!!

# SPECIFICATION FOR APPROVAL

CUSTOMER:  CUSTOMER P/N:  OUR DWG No:  QUANTITY:	0	Pcs.	DATE : _	2013/07/02	
11 = 101 .		GFI	2012121-1	OOIVI-NP	
		CIFICA CEPTEI	_		
COMPONENT ENGINEER					
ELECTRICAL ENGINEER					
MECHANICAL ENGINEER					
APPROVED					
REJECTED					
奇力新電子股份有限公司 CHILISIN ELECTRONICS COI NO.29,LANE 301,TEHHSIN RO HSINCHU,TAIWAN,303, REPUBLIC OF CHINA TEL: (03) 599-2646 FAX: (03) 599-9176 E-mail: Sales@chilisin.com.tw http://www.chilisin.com.tw 台北營業處 Taipei Office 1F., No.2, Aly. 1, Ln. 235, Baoo Xindian Dist., New Taipei City 2 TEL: +886-2-6629-5588~9 FAX: +886-2-6629-0088 E-mail: Sales@chilisin.com.tw	RP. DAD,HUKOU / qiao Rd., 231, Taiwan	Chilis I, No. 7 Qing: TEL FAX E-ma Onliis No.1 Suzh Posta TEL: FAX:	8, Puxing Rd., Yei Town, Donggus +86-769-8773- : +86-769-8773- : +86-769-8773 il : cect@chilisin 新電子(蘇州) in Electronics (S	Dongguan) Co., Ltd. Yuliangwei Administration Area, Juan City, Guangdong, China 0251~3 -0232 n.com.tw )有限公司 Suzhou) Co., Ltd. d., Suzhou New District,	
DRAWN BY 江鳳玉 <b>linda.j</b>				APPROVED BY 詹嘉皓 allen.chan	

- 1 Scope: This specification applies to Multilayer Ferrite chip inductors
- 2 Part Numbering: Product Identification



3 Rating:

**Operating Temperature:**  $-4.0 \, ^{\circ}\text{C} \sim 1.0.5 \, ^{\circ}\text{C}$  (Including self - temperature rise)

Storage Temperature:  $-40 \, ^{\circ}\text{C} \sim 85 \, ^{\circ}\text{C}$  (after PCB)

 $-\,5\,\text{°C}\,{\sim}\,4\,\,0\,\text{°C}$  ,Humidity  $\,4\,\,0\,\%\,{\sim}\,7\,\,0\,\%$  (before PCB)

4 Marking:

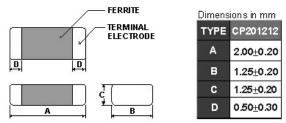
No Marking

### 5 Standard Testing Condition

	Unless otherwise specified	In case of doubt
Temperature	Ordinary Temperature(15 to 35°ℂ)	<b>20±2</b> ℃
Humidity	Ordinary Humidity(25 to 85% RH)	60 to 70 % RH



### 6 Configuration and Dimensions:



### 7 ELECTRICAL CHARACTERISTICS :

Part No.	Inductance	Test Frea.	RDC	Rated Current	Tolerance
Tartito.	(uH)		(Ω)Max.	(mA)Max.	(±%)
CPY201212T-100□-NP	10	1 MHz,200 mV	0.5	400	20,30

NOTE: 

-tolerance M=±20% / T=±30%

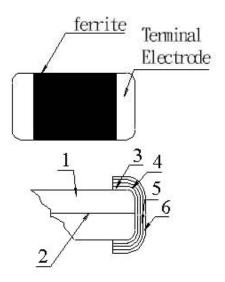
<sup>1.</sup>Operating temperature range  $-~4~0~\% \sim 1~0~5~\%$  (Including self - temperature rise)

<sup>&</sup>quot;-N" FOR COMPLETELY LEAD FREE TYPE(INCLUDING FERRITE BODY & SOLDER)



### 8 CPY201212T Series

### 8.1 Construction:



#### 8.2 Material List:

NO	PART	MATERIAL
1	Ferrite Substance	NiO-CuO-ZnO-Ferrite
2	Silver electrode	Ag
3	Silver electrode	Ag
4	Cu plating	Cu
-5	Ni plating	Ni
6	Sn plating	Sn

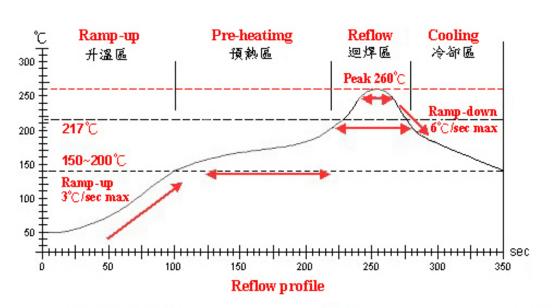


# 9 Reliability Of Ferrite Multilayer Chip Inductor 1-1.Mechanical Performance

No	ltem	Specification	Test Method	
1-1-1	Flexure Strength	The forces applied on the right	Test device shall be soldered on the substrate	
		conditions must not damage	Substrate Dimension: 100x40x1.6mm	
		the terminal electrode and the	Deflection: 2.0mm	
		ferrite	Keeping Time: 30sec	
			*For 100505, substrate dimension is 100x40x0.8mm	
1-1-2	Vibration		Test device shall be soldered on the substrate	
			Oscillation Frequency: 10 to 55 to 10Hz for 1min	
			Amplitude: 1.5mm	
			Time: 2hrs for each axis (X, Y & Z), total 6hrs	
1-1-3	Resistance to Soldering Heat		Pre-heating: 150℃, 1min	
		More than 75% of the terminal	Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free)	
		electrode should be covered	Solder Temperature: 260±5℃	
		with solder.	Immersion Time: 10±1sec	
		Inductance: within ±20% of		
		initial value		
1-1-4	Solder ability	The electrodes shall be at	Pre-heating: 150°C, 1min	
		least 95% covered with new	Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free)	
		solder coating	Solder Temperature: 245±5°C (Pb-Free)	
			Immersion Time: 4±1sec	

No	Item	Specification	Test Method			
1-2-1	Temperature Cycle	Appearance: No damage	One cycle:	)ne cycle:		
		Inductance:within±20% of	Step	Temperature (°ℂ)	Time (min)	
		initial value	1	-40±3	30	
			2	25±2	3	
			3	105±3	30	
			4	25±2	3	
			Total: 100cycles  Measured after exposure in the room condition for			
1-2-2	Humidity Resistance		Temperature: 40±2°C Relative Humidity: 90 ~ 95% / Time: 1000hrs			
			Measured after exposure in the room condition for			
1-2-3	High		Temperature: 85±3°C			
	Temperature Resistance		Relative Humidity: 20%			
			Applied Current: Rated Current / Time: 1000hrs Measured after exposure in the room condition for 24			
1-2-4	Low		Temperature: -40±3°C			
	Temperature Resistance Relative Humidity: 0% / Time: 1000hrs					
			Measured after exposure in the room condition for 24hrs			





#### Lead-Free(LF) 標準溫度分析範圍

Refer to J-STD-020C

管制項目 Item.	升溫區 Ramp-up	預熱區 Pre-heatimg	迴焊區 Reflow	Peak Temp	冷部區 Cooling
溫度範圍 Temp.scope	R.T. ~150°C	150°C ~ 200°C	217℃	260±5°C	Peak Temp. ~ 150°C
標準時間 Time spec.	_	60 ~ 180 sec	60 ~ 150 sec	20 ~ 40 sec	_
實際時間 Time result	7 <u>2</u>	75 ~ 100 sec	90 ~ 120 sec	20 ~ 35 sec	_

#### NOTE:

- 1. Re-flow possible times: within 2 times
- 2. Nitrogen adopted is recommended while in re-flow



# 11 PACKAGING

### 11.1 Packaging -Cover tape

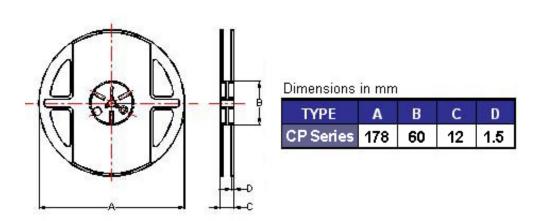
The force for tearing off cover tape is 10 to 100 grams in the arrow direction.



### 11.2 Packaging Quantity

TYPE	BULK	PCS/REEL
CP160808	<b>✓</b>	4000
CP201209	✓	4000
CP201212	<b>✓</b>	3000
CP 32 1611	<b>√</b>	3000

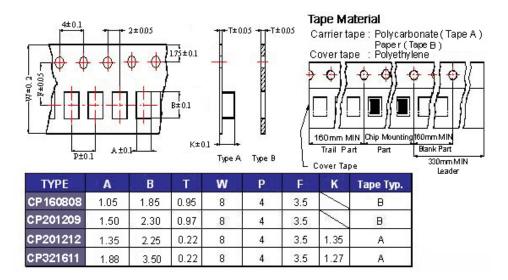
#### 11.3 Reel Dimensions



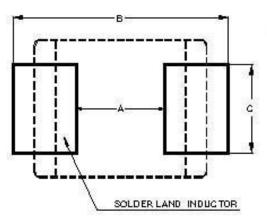


# 11 PACKAGING

#### 11.4 Tape Dimensions in mm



## 12 Recommended Pattern



#### Dimensions in mm

TYPE	Α	В	С
CP160808	0.7 ~ 0.8	1.8 ~ 2.0	0.6 ~ 0.8
CP201209	1.0 ~ 1.2	2.6 ~4.0	1.0 ~1.2
CP201212	1.0 ~ 1.2	2.6 ~4.0	1.0~1.2
CP321611	2.0	4.2~5.2	1.2

### 13 Note:

- 1. Please make sure that your product is has been evaluated and confirmed against your specifications when our product is mounted to your product.
- 2. Do not knock nor drop.
- 3. All the items and parameters in this product specification have been prescribed on the premise that our product is used for the purpose, under the condition and in the environment agreed upon between you and us. You are requested not to use our product deviating from such agreement.
- 4. Please keep the distance between transformer/coil and other components (refer to the standard IEC 950)



# 14 Curve:

