



ISO9001 & ISO14001 & TS16949 **CHILISIN ELECTRONICS CORP.**

Lead-Free & RoHs Compliance!!

SPECIFICATION FOR APPROVAL

CUSTOMER : _____

CUSTOMER P/N : _____

OUR DWG No : _____

QUANTITY : **0** **Pcs.** **DATE :** **2013/01/10**

ITEM : **CPY160808T-2R2M-NP**

SPECIFICATION ACCEPTED BY:	
COMPONENT ENGINEER	
ELECTRICAL ENGINEER	
MECHANICAL ENGINEER	
APPROVED	
REJECTED	

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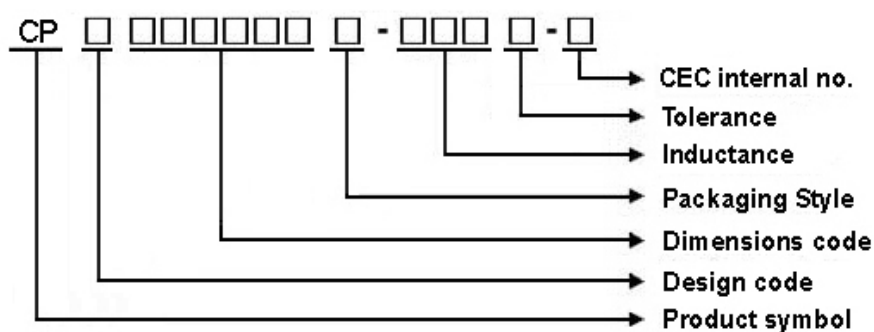
YG13100676



CPY160808T Series Specification

1 Scope: This specification applies to Multilayer Ferrite chip inductors

2 Part Numbering: Product Identification



3 Rating:

Operating Temperature: $-40^{\circ}\text{C} \sim 105^{\circ}\text{C}$ (Including self - temperature rise)

Storage Temperature: $-40^{\circ}\text{C} \sim 85^{\circ}\text{C}$ (after PCB)

$-5^{\circ}\text{C} \sim 40^{\circ}\text{C}$, Humidity 40% ~ 70% (before PCB)

4 Marking:

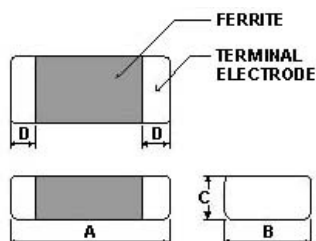
No Marking

5 Standard Testing Condition

	Unless otherwise specified	In case of doubt
Temperature	Ordinary Temperature(15 to 35°C)	20±2°C
Humidity	Ordinary Humidity(25 to 85% RH)	60 to 70 % RH

CPY160808T Series Specification

6 Configuration and Dimensions:



Dimensions in mm

TYPE	CPY160808
A	1.6±0.2
B	0.8±0.2
C	0.8±0.2
D	0.3±0.2

7 ELECTRICAL CHARACTERISTICS :

Part No.	Inductance (uH)	Test Freq.	RDC (Ω)Max.	Rated Current (mA)Max.	Tolerance (±%)
CPY160808T-1R0□-NP	1	1 MHz,200 mV	0.12	1500	20,30
CPY160808T-2R2□-NP	2.2	1 MHz,200 mV	0.2	1000	20,30
CPY160808T-4R7□-NP	4.7	1 MHz,200 mV	0.25	800	20,30
CPY160808T-100□-NP	10	1 MHz,200 mV	0.9	90	20,30

NOTE: □-tolerance M=±20% / T=±30%

1.Operating temperature range – 4 0℃ ~ 1 0 5℃(Including self - temperature rise)

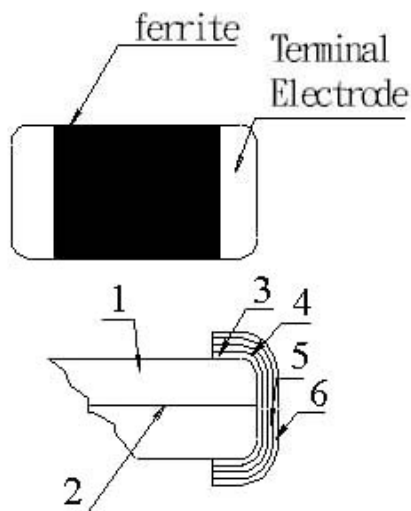
2.Rate Current : Applied the current to coils, the temperature rise shall not be more than 30℃

"-N" FOR COMPLETELY LEAD FREE TYPE(INCLUDING FERRITE BODY & SOLDER)

CPY160808T Series Specification

8 CPY160808T Series

8.1 Construction:



8.2 Material List:

NO	PART	MATERIAL
1	Ferrite Substance	NiO-CuO-ZnO-Ferrite
2	Silver electrode	Ag
3	Silver electrode	Ag
4	Cu plating	Cu
5	Ni plating	Ni
6	Sn plating	Sn



CPY160808T Series Specification

9 Reliability Of Ferrite Multilayer Chip Inductor

1-1.Mechanical Performance

No	Item	Specification	Test Method
1-1-1	Flexure Strength	The forces applied on the right conditions must not damage the terminal electrode and the ferrite	Test device shall be soldered on the substrate Substrate Dimension: 100x40x1.6mm Deflection: 2.0mm Keeping Time: 30sec *For 100505, substrate dimension is 100x40x0.8mm
1-1-2	Vibration		Test device shall be soldered on the substrate Oscillation Frequency: 10 to 55 to 10Hz for 1min Amplitude: 1.5mm Time: 2hrs for each axis (X, Y & Z), total 6hrs
1-1-3	Resistance to Soldering Heat	Appearance: No damage More than 75% of the terminal electrode should be covered with solder. Inductance: within $\pm 20\%$ of initial value	Pre-heating: 150°C, 1min Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free) Solder Temperature: 260 ± 5 °C Immersion Time: 10 ± 1 sec
1-1-4	Solder ability	The electrodes shall be at least 95% covered with new solder coating	Pre-heating: 150°C, 1min Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free) Solder Temperature: 245 ± 5 °C (Pb-Free) Immersion Time: 4 ± 1 sec

1-2.Environmental Performance

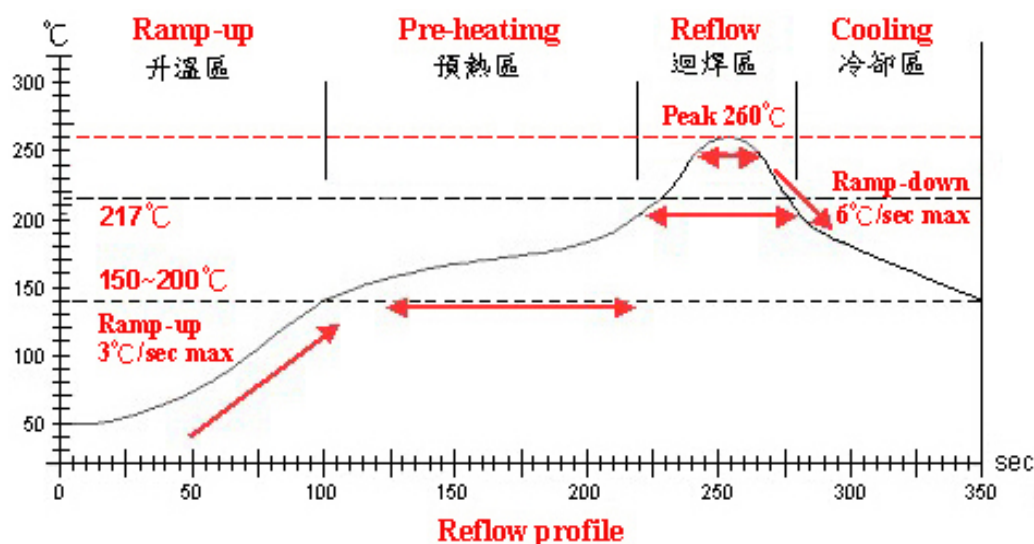
7. Environmental Performance					
No	Item	Specification	Test Method		
1-2-1	Temperature Cycle	Appearance: No damage Inductance: within±20% of initial value	One cycle:		
			Step	Temperature (°C)	Time (min)
			1	-40±3	30
			2	25±2	3
			3	105±3	30
			4	25±2	3
			Total: 100cycles Measured after exposure in the room condition for 24hrs		
1-2-2	Humidity Resistance		Temperature: 40±2℃ Relative Humidity: 90 ~ 95% / Time: 1000hrs Measured after exposure in the room condition for 24hrs		
1-2-3	High Temperature Resistance		Temperature: 85±3℃ Relative Humidity: 20% Applied Current: Rated Current / Time: 1000hrs Measured after exposure in the room condition for 24hrs		
1-2-4	Low Temperature Resistance		Temperature: -40±3℃ Relative Humidity: 0% / Time: 1000hrs Measured after exposure in the room condition for 24hrs		



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CPY160808T Series Specification



Lead-Free(LF) 標準溫度分析範圍

Refer to J-STD-020C

管制項目 Item.	升温區 Ramp-up	預熱區 Pre-heating	迴焊區 Reflow	Peak Temp	冷卻區 Cooling
溫度範圍 Temp.scope	R.T. ~ 150°C	150°C ~ 200°C	217°C	260±5°C	Peak Temp. ~ 150°C
標準時間 Time spec.	—	60 ~ 180 sec	60 ~ 150sec	20 ~ 40 sec	—
實際時間 Time result	—	75 ~ 100 sec	90 ~ 120sec	20 ~ 35 sec	—

NOTE :

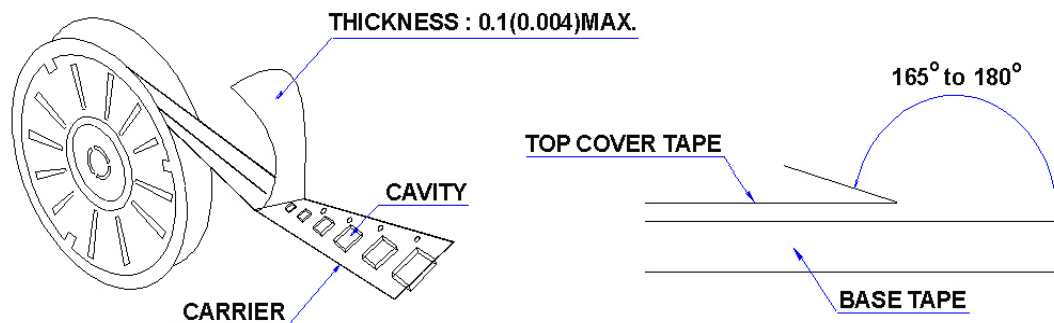
1. Re-flow possible times : within 2 times
2. Nitrogen adopted is recommended while in re-flow

CPY160808T Series Specification

11 PACKAGING

11.1 Packaging -Cover tape

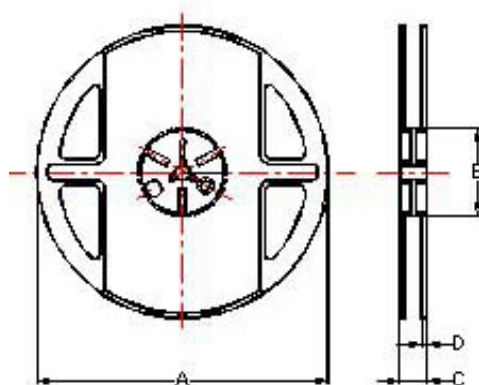
The force for tearing off cover tape is 10 to 100 grams in the arrow direction.



11.2 Packaging Quantity

TYPE	BULK	PCS/REEL
CP160808	✓	4000
CP201209	✓	4000
CP201212	✓	3000
CP321611	✓	3000

11.3 Reel Dimensions



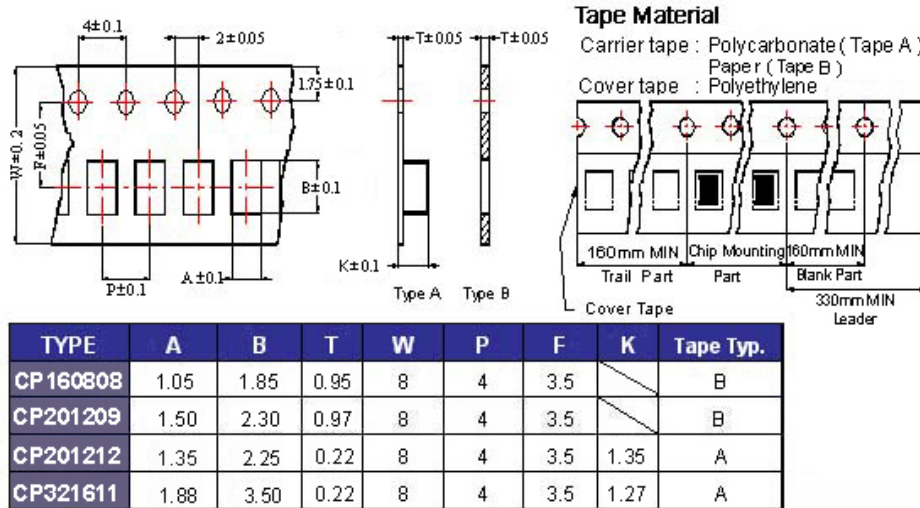
Dimensions in mm

TYPE	A	B	C	D
CP Series	178	60	12	1.5

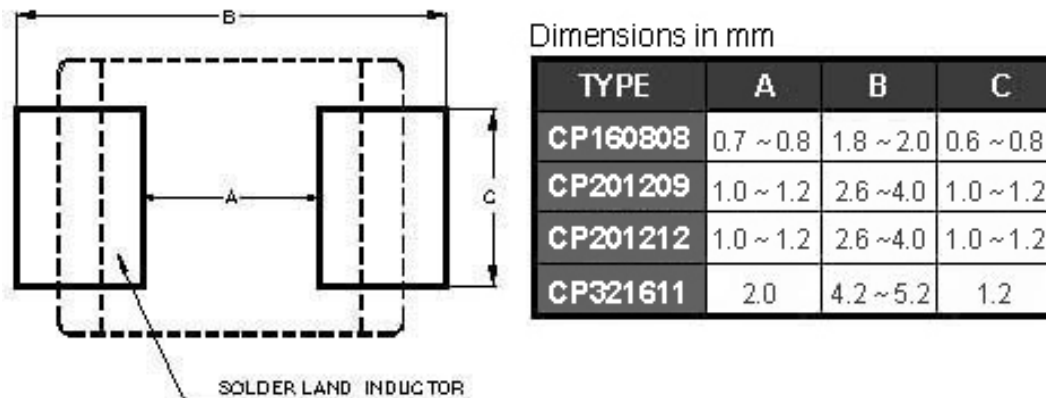
CPY160808T Series Specification

11 PACKAGING

11.4 Tape Dimensions in mm



12 Recommended Pattern



13 Note:

1. Please make sure that your product is has been evaluated and confirmed against your specifications when our product is mounted to your product.
2. Do not knock nor drop.
3. All the items and parameters in this product specification have been prescribed on the premise that our product is used for the purpose, under the condition and in the environment agreed upon between you and us. You are requested not to use our product deviating from such agreement.
4. Please keep the distance between transformer/coil and other components (refer to the standard IEC 950)



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14 Curve:

