

#### Lead-Free & RoHs Compliance!!

### **SPECIFICATION FOR APPROVAL**

CUSTOMER	•
CUSIOWER	•

CUSTOMER P/N :

OUR DWG No:

QUANTITY :

Pcs. DATE :

2013/05/29

ITEM :

0

LVF505020-SERIES

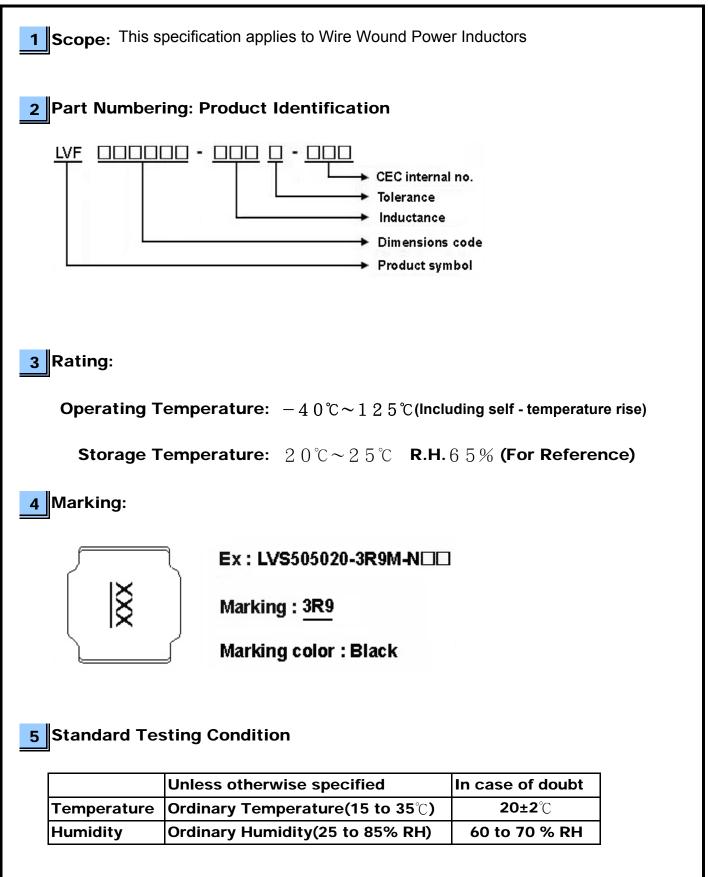
		IFICATION EPTED BY:
COMPONENT		
ENGINEER		
ELECTRICAL		
ENGINEER		
MECHANICAL		
ENGINEER	<u>_</u>	
APPROVED		
REJECTED		
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http://www.chilisin.com.tw 台北營業處 Taipei Office 1F., No.2, Aly. 1, Ln. 235, Baoo Xindian Dist., New Taipei City 2 TEL:+886-2-6629-5588~9 FAX:+886-2-6629-0088		奇力新電子(蘇州)有限公司 Chilisin Electronics (Suzhou) Co., Ltd. No.143,Song Shan Rd., Suzhou New District, Suzhou,China Postal Code:215129 TEL:+86-512-6841-2350 FAX:+86-512-6841-2356

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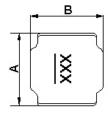
### **LVF505020 Series Specification**



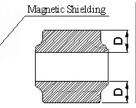


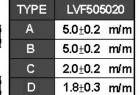
## **LVF505020 Series Specification**

#### 6 Configuration and Dimensions:









### 7 ELECTRICAL CHARACTERISTICS :

Part No.	Inductance (uH)	Test Freq.	RDC (mΩ)±30%	lsat(A) Typ.(Max)	Irms(A) Typ.(Max)	Tolerance (±%)	Marking
LVF505020-1R0□-NCO	1	100kHz,1V	18	6.0(5.40)	4.1(3.69)	20,30	1R0
LVF505020-1R5□-NCO	1.5	100kHz,1V	23	4.9(4.41)	3.5(3.15)	20,30	1R5
LVF505020-2R2 -NCO	2.2	100kHz,1V	30	4.0(3.60)	3.3(2.97)	20,30	2R2
LVF505020-3R9□-NCO	3.9	100kHz,1V	53	2.9(2.61)	2.6(2.34)	20,30	3R9
LVF505020-4R7 NCO	4.7	100kHz,1V	60	2.7(2.43)	2.2(1.98)	20,30	4R7
LVF505020-6R8 -NCO	6.8	100kHz,1V	93	2.2(1.98)	1.8(1.62)	20,30	6R8
LVF505020-100□-NCO	10	100kHz,1V	125	1.8(1.62)	1.6(1.44)	20,30	100
LVF505020-150 -NCO	15	100kHz,1V	195	1.4(1.26)	1.2(1.08)	20,30	150
LVF505020-220 - NCO	22	100kHz,1V	265	1.2(1.08)	1.0(0.90)	20,30	220

NOTE: 
--tolerance M=±20% / T=±30%

1.Operating temperature range  $-~4~0~{}^\circ\!{\rm C}\sim 1~2~5~{}^\circ\!{\rm C}$  (Including self - temperature rise)

2.Isat for Inductance drop 30% from its value without current.

3.Irms for a 40  $^\circ\!\mathrm{C}$  rise above 25  $^\circ\!\mathrm{C}$  ambient.

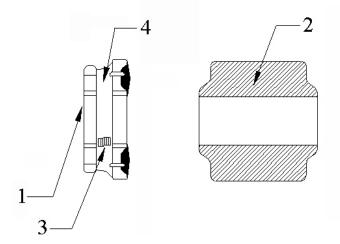
"-N" FOR COMPLETELY LEAD FREE TYPE(INCLUDING FERRITE BODY & SOLDER)



## **LVF505020 Series Specification**

8 LVF505020 Series

8.1 Construction:



#### 8.2 Material List:

ITEM	PART	DESCRIPTION	SUPPLIES
1	CORE	FERRITE	CHILISIN
2	TERMINAL	Ag/Ni/Sn	
3	WIRE	Grade 180	ELEKTRISOLA
4	EPOXY	Magnetic powder resin	



## **LVF505020 Series Specification**

# 9 Reliability Of Wire Wound Power Inductors -1.Mechanical Performance

	Item	Specification	Test Method
-1-1	Bending Test	Chip coil shall not be	Substrate:Glass-epoxy substrate(100mm*40mm*1.6mm)
		damaged after tested as test	speed of Applying Force:1mm/s
		method	
			Hold Duration:30s
			Leflection
1-1-2	Vibration		Oscillation Frequency:10Hz to 55 Hz to 10 hZ for 1 min Total Amplitude:1.5mm
			Testing Time: A period of 2 hours in each of 3 mutually
			perpendicular directions(Total 6 hours)
1-1-3	Solderability	The wetting area of the	Solder:Sn/Ag3.0/Cu0.5
	-	electrode shall be at least	per-Heating:150℃±10℃/1min to 2min
		95% covered with new solder	solder Temperature:245°C±5°C
		coating	Immersion Time:4s±1s
1-1-4	Resistance to	Appearance:No damage	Solder:Sn/Ag3.0/Cu0.5
	Soldering Heat		per-Heating:150°C±10°C/1min to 2min solder Temperature:260°C±5°C
			Immersion Time:10s±1s
1-1-5	Resistance to solvent	There must be no change in	Inductors must withstand 6 minutes of alcohol or water.
		appearance or obliteration of	
		marking.	
1-2.E	nvironmental Perfo	rmance	
No	Item	Specification	Test Method
1-2-1	Heat Resistance	Appearance: No damage	Temperature:125℃±3℃
		Inductance Change:within±10%	Time:500h
			Then measured after exposure in the room Condition for 24h±2h
1-2-2	Cold Resistance	4	Temperature: -40°C±3°C
· <u> </u>			Time:500h
			Then measured after exposure in the room
			Condition for 24h±2h
1-2-3	Humidity		Temperature: 40°C ±2°C
			Humidity:90%(RH) to 95%(RH)
			Time:500h
			Then measures after exposure in the room
			Condition for 2/h+2h
1-2-4	Temperature Cycle	4	Condition for 24h±2h One cycle:

Step

1 2

3

4

Total: 100cycles

Temperature (°C

-40±3

25±2

125±3

25±2

Measured after exposure in the room condition for 24hrs

Time (min) 30

3

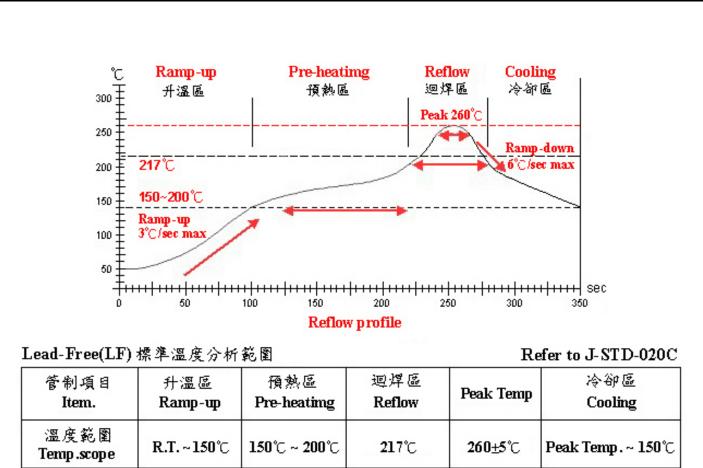
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## **LVF505020 Series Specification**



Temp.scope					
標準時間 Time spec.	_	60 ~ 180 sec	60 ~ 150sec	20 ~ 40 sec	—
實際時間 Time result		75 ~ 100 sec	90 ~ 120 sec	5 ~ 10 sec	-

NOTE :

1. Re-flow possible times : within 2 times

2. Nitrogen adopted is recommended while in re-flow

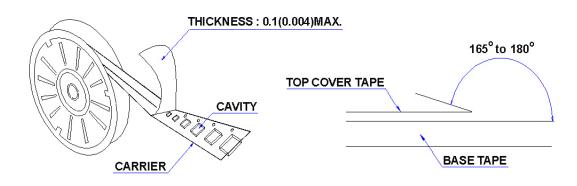


## **LVF505020 Series Specification**

### 11 PACKAGING

#### 11.1 Packaging -Cover tape

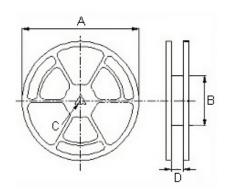
The force for tearing off cover tape is 10 to 130 grams in the arrow direction.



#### **11.2 Packaging Quantity**

ТҮРЕ	BULK	PCS/REEL
LVF505020	1	2000

#### **11.3 Reel Dimensions**



Reel Dimension :	m/m			i and a star
TYPE	А	В	С	D
LVF505020	330	100	13	13.4

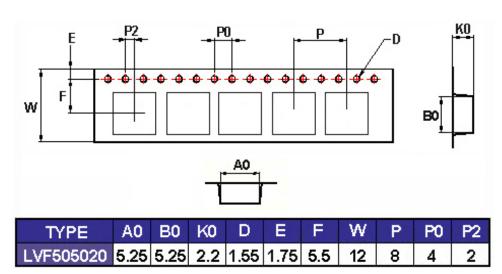


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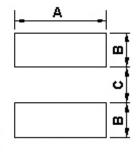
## **LVF505020 Series Specification**

### 11 PACKAGING

11.4 Tape Dimensions in mm



#### 12 Recommended Pattern



Dimensions in mm	1		
TYPE	A(m/m)	B(m/m)	C(m/m)
LVF505020	4.2	1.6	2.0

### 13 Note:

- 1. Please make sure that your product is has been evaluated and confirmed against your specifications when our product is mounted to your product.
- 2. Do not knock nor drop.
- 3. All the items and parameters in this product specification have been prescribed on the premise that our product is used for the purpose, under the condition and in the environment agreed upon between you and us. You are requested not to use our product deviating from such agreement.
- 4. Please keep the distance between transformer/coil and other components (refer to the standard IEC 950)



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### **LVF505020 Series Specification**

### 13 Note:

- 5.Storage and Handing Requirements
  - (1)Storage period
    - Use the products within 12 months after delivered
    - Solderability should be checked if this period is exceeded
- (2)Storage conditions
  - \*Products should be stored in the warehouse on the following conditions
  - Temperature: -10°C ~ 40°C

Humidity  $: 30\% \sim 70\%$  relative humidity no rapid change on temperature and humidity The electrode of the products is coated with solder.Don't keep products in corrosive gases such as sulfur, chlorine gas or acid, or it may cause oxidization of electrode, resulting in poor solderability.

\*Products should not be storaged on bulk packaging condition to prevent the chipping of the core and the breaking of winding wire caused by the collision between the products.

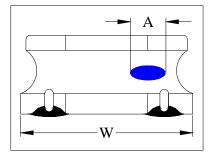
\*Products should be storaged on the palette for the prevention of the influence from humidity, dust and so on.

\*Products should be storaged in the warehouse without heat shock,vibration,direct sunlight and so on.

(3)Handing Condition

Care should be taken when transporting or handing product to avoid excessive vibration or mechanical shock.

6. Void Appearance tolerance Limit



$$\begin{array}{l} A \leq W/2 \text{ GOOD} \\ A > W/2 \text{ NG} \end{array}$$