





**Rear Module Connection Area** 



**Supply Front View** 

Figure 1. LPM409 Expanded Views





### **APPLICATIONS**

- Industrial equipment
- Telecommunications
- Test and measurement
- Automation
- Peripherals
- Audio/broadcast
- Linear and rotary motion

#### **FEATURES**

- Standard output voltages of 2.2 to 51 VDC
- Efficiencies up to 86% typical
- Extra-Low 1U profile: 1.6 inch
- High power density design of 15 Watts/cubic inch
- 1 to 4 isolated outputs with full user configurability
- Power Factor Correction (PFC) IEC 61000-3-2 compliant
- 700W @ 85 VAC or 900W @ 150VAC of total output power
- Zero-load operation
- Single-wire current sharing
- Universal input AC range
- Individual control signals on each module
- Auxiliary power 5V (1A)

### 1. Description

The LPM409 Series is a modular 900-watt AC-DC power supply that provides a market-leading power density of 15 watts per cubic inch and has an extra low 1U profile. The LPM409 offers the flexibility of a modular architecture and the combination of high efficiency and high power density.

Designed for use where a unique set of voltage and current requirements are needed, the supply's four slots can be configured with PCB-based output modules to deliver up to four outputs. The LPM output modules operate in any chassis position and can provide up to 900 watts total power from a 150 VAC input and 700 watts from a 85 VAC input. Forced-air cooling with airflow direction from front to back is provided by an internal fan.

For LPM409 supplies using less than the four-slot capability, blanking plates are installed for safety purposes and to optimize airflow within the chassis. The supplies are pre-set with default output module settings or with the customer's desired output settings prior to delivery. The LPM409 chassis can be populated with the output modules listed in *Table 1*.

Table 1. Module Selection

| MODULE | NO. OF<br>SLOTS<br>REQUIRED | FACTORY-SET<br>SINGLE-OUTPUT<br>(VDC) | MODULE<br>ADJUSTABLE RANGE<br>(VDC) | OUTPUT CURRENT 2)<br>(MAX AMPS) | OUTPUT POWER 2)<br>(MAX WATTS) |
|--------|-----------------------------|---------------------------------------|-------------------------------------|---------------------------------|--------------------------------|
| А      | 1                           | 5                                     | 2.2 to 5.2                          | 50                              | 250                            |
| В      | 1                           | 12                                    | 5.2 to 15                           | 20                              | 250                            |
| С      | 1                           | 24                                    | 15 to 32                            | 10                              | 250                            |
| D      | 1                           | 48                                    | 32 to 51                            | 8                               | 250                            |
| 0      | 1                           | Blank Panel Slot Cover                |                                     |                                 |                                |

**NOTE 1:** To determine your desired power supply's part number, please refer to *Figure 2* for a detailed part number description. Use the **LPM615 Product Configurator** located in the Modular Section on our website, or use our contacts page to locate a contact or distributor for further assistance.

**NOTE 2:** Values given for high line, from 150 VAC. By the full configuration (all 4 modules full loaded) is the module output power derated to 175/225 W (low line/high line).

### 1.1 Output Voltage and Current Limit Adjustments

Each LPM409 module's output voltage and current limit is adjustable by means of a trimmer located on the module and accessible through the adjustment holes located on the bottom of the power supply (see *Figure 10*). We dissuade to change current limits, which are factory preset.

#### 1.2 Parallel Connections

Depending on certain situations where current requirements exceed one module's capability, the configurator will process a solution using parallel connections. Paralleling busbars are available to make the connections requiring higher current needs. For proper current sharing function it is necessary to interconnect Current Share pins of all modules by signal wires. For more details read the *Output Signal Connector* and *Accessories* section.



### 2. Part Numbering and Ordering Information

#### 2.1 Chassis Identification Numbering

First left-to-right sequence of the part number (4 characters):

LPM409 ..... Low Profile Modular 4-slot, 900W, with single-phase AC input. (see Figure 2)

Note: Chassis and modules are RoHS-6 compliant.

#### LPM 4-SLOT MODEL PART NUMBER DESCRIPTION INFORMATION

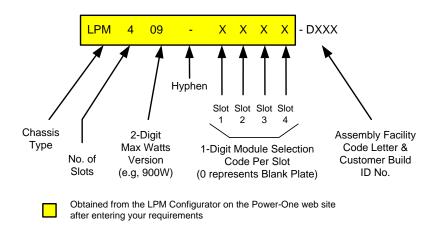


Figure 2. LPM 4-Slot Part Number Structure

**Example**: After entering your requirements, the web Configurator optimized part number recommended could be: LPM409-0DAC-DXXX which represents: Low Profile Modular Series with single-phase AC input, 4-slot, 900W chassis with modules of: a blank panel, modules D, A, C in slots 1 to 4, respectively. Besides the blank panel in Slot 1, the example's modules in this case would represent desired DC output voltages as follows: Slot 2 = D for 48V. Slot 3 = A for 3.3V and Slot 4 = C for 24V.

Assembly facility code and customer build ID No. are established during actual power supply assembly.

### 3. Safety specifications

### 3.1 Safety approvals

- UL/CSA 60950-1, 2<sup>nd</sup> edition
- IEC 60950-1:2005
- CE Mark for LVD









### 3.2 Insulation Safety Ratings

| TEST POINTS        | MINIMUM TEST VOLTAGE | INSULATION SAFETY RATING |
|--------------------|----------------------|--------------------------|
| Input-to-Chassis   | 2500Vdc / min. 1s    | Basic Insulation         |
| Input-to-Output    | 4240Vdc / min. 1s    | Reinforced Insulation    |
| Outputs-to-Chassis | 50Vdc / min. 1s      | Functional Insulation    |
| Output-to-Output   | 50Vdc / min. 1s      | Functional Insulation    |



# 4. Environmental and Reliability specifications

4.1 Environmental specification

| PARAMETER                | PARAMETER CONDITIONS / DESCRIPTION   |     | NOM. | MAX. | UNITS |
|--------------------------|--|-----|------|------|-------|
| Cooling                  | Internal DC fan, airflow is from the front of the supply and exhausts from the rear connector area           |     |      |      |       |
| Audible Noise            | Single unit, 4 modules assembled, on a table at 1m distance  |     |      | 65   | dBA   |
| Operating<br>Temperature | Full power; derated linearly from 100% load @ 50°C to 50% load @ 70°C  | -20 | 25   | 50   | °C    |
| Storage Temperature      |  | -40 |      | 75   | °C    |
| Humidity                 | 95% relative humidity @ 25 °C, non-condensing  |     |      |      |       |
| Vibration                | Operational: Sine 5Hz, 250Hz, 500Hz profile, 3 axes, 90min / axis Non-operating: 20Hz, 350Hz, 500Hz profile, |     |      | 0.5  | Grms  |
|                          | 3 axis, 10min / axis   |     |      | 3.13 | Grms  |
| Shock                    | Non operational: 11ms, +/- 5 half sinus, 3 axes, total of 10 shocks  |     |      | 40   | Gpk   |

4.2 Reliability

| PARAMETER         | CONDITIONS / DESCRIPTION                      | MIN.    | NOM. | MAX. | UNIT<br>S |
|-------------------|---|---------|------|------|-----------|
| Calculated MTBF   | According to MIL-HDBK217, Ground benign 30 °C | 250,000 |      |      | hours     |
| Demonstrated MTBF | Tamb=25°C                                     | 250,000 |      |      | hours     |

### 5. Fault Protection

| PARAMETER                        | CONDITIONS / DESCRIPTION  | MIN.                            | NOM. | MAX. | UNITS    |
|----------------------------------|---|---------------------------------|------|------|----------|
| Input Fuse                       | One fuse, non-user serviceable, located on line leg of AC input, Fast Acting type |                                 |      | А    |          |
| Inrush Current Limitation        | Provided by NTC   | See Input specification section |      | tion |          |
| Short Circuit Protection         | Provided by Current Limit circuit   |                                 |      | 150  | % lo_nom |
| Output Overvoltage<br>Protection | Hiccup on 5 and 12V Modules<br>Latching on 24V and 48V modules                    | 10                              |      | 25   | % Vo_nom |
| Over Temperature                 | By nominal load, OT with auto restart   |                                 | 60   | 65   | °C amb   |



# 6. EMC specifications

6.1 EMC Immunity

| PARAMETER                     | CONDITIONS / DESCRIPTION   | CRITERION            |
|-------------------------------|--|----------------------|
| Electrostatic Discharge (ESD) | IEC/EN61000-4-2; GR-1089 R2-1, R2-2, R2-3<br>Level 4: contact: ±8 kV, air: ± 15kV  | Perform, criterion B |
| RF Susceptibility             | IEC/EN 61000-4-3;<br>Level 3: 10 V/m; 80 MHz to 1000 MHz;<br>AM 80%, 1 kHz radiated; RF electromagnetic field                      | Perform, criterion A |
| Fast transient / burst        | IEC/EN 61000-4-4; Level 3; ±2 kV, 5kHz electrical fast transient / burst immunity test   | Perform, criterion B |
| Surge                         | IEC61000-4-5, level 3; line to earth: ± 2kV, line to line: ± 1kV surge immunity test   | Perform, criterion B |
| RF conducted disturbance      | IEC/EN 61000-4-6; Level 3; GR-108;<br>10V, 0.15 to 80MHz, AM 80%, 1kHz   | Perform, criterion A |
|                               | IEC/EN 61000-4-11; Voltage dips, interruptions and variations. (Interpretation: dip below Vi min with Po nom = hold-up time 10 ms) |                      |
|                               | 1a: Dip 30%, 100ms   | Perform, criterion B |
| Voltage dips / short          | 1a: Dip 30%, 200ms   | Perform, criterion B |
| interruptions                 | 1a: Dip 60%, 10ms  | Perform, criterion A |
|                               | 1a: Dip 60%, 100ms   | Perform, criterion B |
|                               | 1a: Dip >95%, 10ms (interruption)  | Perform, criterion A |
|                               | 1a: Dip >95%, 100ms (interruption)   | Perform, criterion B |

## 6.2 EMC Emission

| PARAMETER                          | CONDITIONS / DESCRIPTION   | CRITERION                                      |
|------------------------------------|--|--|
| Conducted Emission<br>EN55022, FCC | EN 55022, FCC – EN 55022, and CISPR 22 Class B, FCC 47CFR15 unintentional radiators; standalone at all nominal input voltages and measured in Po1: 0, 50%, 100%; signal connections open | Class B;<br>min. 3dB/μV margin                 |
| Radiated Emission<br>EN55022, FCC  | EN 55022, FCC – EN 55022, and CISPR 22 Class B, FCC 47CFR15 unintentional radiators; standalone at all nominal input voltages and measured in Po1: 0, 50%, 100%; signal connections open | Radiated Class A QSP / AVG<br>min. 3 dB margin |
| Input Current<br>Harmonics         | EN 61000-3-2, sine wave input, Class D; measured standalone at all Vin nominal at power levels between 0 and rated   | Pass   |

# 7. Electrical specifications

### 7.1 Input Specifications

| PARAMETER                  | CONDITIONS / DESCRIPTION   | MIN.           | NOM.  | MAX.  | UNITS       |
|----------------------------|--|----------------|-------|-------|-------------|
| Input AC Voltage<br>Range  | Continuous AC voltage Range<br>Po=700W from 85-150 VAC<br>Po=900W from 150-264 VAC                                       | 85 115/230 264 |       | 264   | VAC         |
| Input Overvoltage<br>Range | At max. power, no input OVP shutdown   |                |       | 300   | VAC         |
| Ground continuity          |  |                |       | 0.1 1 | Ohm         |
| Leakage Current            | @ 264 VAC, 60Hz, Standard Commercial   |                |       | 1.5   | mA          |
| Frequency                  | AC line  | 47             | 50/60 | 63    | Hz          |
| Power Factor               | Active PFC meets requirements of EN 61000-3-2 at full load ,120/230 VAC input  | 0.98           |       |       |             |
| Input Current              | Steady state, 85 VAC at 700W   |                | 11    |       | Amps<br>rms |
| Inrush Current             | 180 VAC, Max Power, 25°C,<br>acc. prETS300-132-1<br>230 VAC, Max Power, 25°C,<br>acc. prETS300-132-1 (Repeat rate >1min) |                | 40    |       | A pk        |
|                            | Vi = 230 VAC 100% loading  | 86             | 87    |       | %           |
| Efficiency <sup>2</sup>    | Vi = 230 VAC 30-80% loading  | 81             | 82    |       | %           |
| Emcleticy                  | Vi = 115 VAC 100% loading  | 84             | 85    |       | %           |
|                            | Vi = 115 VAC 30-80% loading  | 78             | 81    |       | %           |

<sup>&</sup>lt;sup>1</sup> For any combination of output modules, any valid load and voltage setting

<sup>&</sup>lt;sup>2</sup> Efficiency typical for standard configuration ABCD and nominal output voltage settings



## 7.2 Output Specifications

| PARAMETER                       | CONDITIONS/DESCRIPTION  | MIN.                      | NOM.               | MAX.            | UNITS    |
|---------------------------------|---|---------------------------|--------------------|-----------------|----------|
| Output Power                    | One fan for internal cooling  |                           | 900                |                 | W        |
| Output DC<br>Voltages / Modules | All output modules work in any chassis position and are max 1U high PCB-based   |                           |                    | -               |          |
| Current Share                   | Active single-wire current share.  Maximum difference in currents between two modules – percentage of one module nominal current. | ±10                       |                    | ±10             | %        |
| Line Regulation                 | Input from 85 to 264 VAC , 80% load   |                           | 1.0                |                 | % Vo_nom |
| Load Regulation                 | From 0-100% load, Input >180VAC, Vo_nom   |                           | 1.0                |                 | % Vo_nom |
| Thermal Drift                   | After 15 minute warm-up period  |                           | 0.02               |                 | %/°C     |
| Total Regulation                | Variation of line, load and temperature drift   |                           | 2.0                |                 | % Vo_nom |
| Output Adjustment<br>Range      |   | S                         | ee <i>Module</i> S | election Tab    | le 1     |
|                                 | Deviation for 10-90% or 90-10% load changes at a rate of 1A/µs, (constant current mode, Vo reach 1% band around Voset)            |                           |                    | 4%<br>2000μs    | -        |
| Dynamic Response                | Deviation for 50-100% or 100-50% load steps with 1A/µs rate. (constant current mode, Vo reach 1% band around Voset)               |                           |                    | 3%<br>400µs     | -        |
| Output Ripple &<br>Noise        | BW = 20MHz; Filter 10nF/10uF; over line and load, 25°C  |                           |                    | 1% of<br>Vo_nom | mVpk-pk  |
| CM Noise                        | Output to chassis, over line and load<br>(Measured across 50 Ohms, with 10µH / 10nF<br>in parallel)                               | 220 500                   |                    | mVpp            |          |
| Overshoot                       | Output voltage overshoot at turn-on   |                           | 4                  |                 | % Vo_nom |
| Turn-On<br>Characteristics      | Turn ON at minimum and nominal output current   | Monoto                    | onous charac       | cteristic       | -        |
| Turn-Off<br>Characteristics     | Turn OFF at minimum and nominal output current  | Monotonous characteristic |                    | -               |          |
| Turn-On Time                    | Time required for output within regulation after initial application of AC input  |                           |                    | 1.5             | s        |
| Turn-On Time                    | Time required for output within regulation after removing inhibit   |                           |                    | 100             | ms       |
| Hold-up Time                    | Vo is required to stay within 95% regulation after AC is removed. Measured from the last AC peak, VAC min and full load.          | 10                        |                    |                 | ms       |
| Remote Sense                    | Total compensation for cable losses   |                           | 250                | 500             | mV       |

7.3 Input - Signals, Features and Indicators description

| Signal Name                                  | Pin                      | Description  |
|--|--------------------------|--|
| Auxiliary Output                             | 1,2 vs<br>3,4<br>(RTN_D) | Output present when voltage of AC line is over 85VAC. Standby nominal voltage is 5.0V, nominal current is 1.0A. Current protected output over point 1.5A. When Standby output is shorted, the primary LED indicators are OFF. Pin referenced to logic return RTN_D.  |
| Input AC Low<br>Indication<br>- PFAIL -      | 5                        | Open Collector output pin with 20mA pull-down capability referenced to logic return RTN_D (TTL logic level).  PFAIL OPEN OR HIGH state indicates the warning that the input has failed 5ms before the output goes below the lower regulation limit. PFAIL will turn-off the green Input OK LED.  PFAIL LOW state indicates that the input voltage is within the operation range.  The FAIL condition occurs when the input voltage falls below 75Vac and remains until the input voltage reaches min. 85Vac. |
| Fan Fail / OTP<br>Indication<br>- FAN_FAIL - | 6                        | Open Collector output pin with 20mA pull-down capability referenced to logic return RTN_D (TTL logic level).  FAN_FAIL OPEN OR HIGH state indicates the fan fail / over temperature condition 100ms before the unit shuts-down. A fan fail / OTP will turn-off the green FAN OK LED.  FAN_FAIL LOW state indicates normal fan operation, no OTP.   |

7.4 Output - Signals, Features and Indicators description

| Signal Name                      | Pin | Description  |
|----------------------------------|-----|--|
| Output Good indication - PG_HI - | 5   | Open collector output with 5mA pull-down capability protected by 5.1V zener diode. Referenced to PG_LO. PG_HI OPEN OR HIGH state indicates that the module output voltage is below lower regulation limit. A PG_HI fail state turns the GREEN DC OK LED to RED. PG_HI LOW state indicates that the module output voltage is above the lower regulation limit.  |
| Output Enable function - EN_HI - | 6   | Input internally pulled up and protected by 5.1V zener diode. Pin sourcing capability is 2mA referenced to EN_LO.  EN_HI OPEN OR HIGH state ENABLES the module output.  EN_HI LOW state INHIBITES the module output.   |
| Positive sense wire - VS+ -      | 1   | Output voltage sense wire. Internally connected to Vout+ via 51 $\Omega$ . It is recommended to connect sense wire at positive load point.   |
| Negative sense<br>wire<br>- VS   | 3   | Output voltage sense wire. Internally connected to Vout- via 51 $\Omega$ . It is recommended to connect sense wire at negative load point.   |
| Current share<br>- CS -          | 4   | Common wire for parallel connected modules to achieve proper current sharing between the modules. Referenced to PG_LO or EN_LO. Interconnect CS pin on all parallel working modules. Active current share pin enables control of output voltage. Pulling-up this pin to 5V is possible to increase the output voltage. Pull-down of this pin has no effect. Voltage on this pin is 3.0V at nominal module current. |

### 7.5 Visual Alarms description

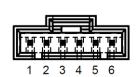
| Alarm Name    | LED position                                | Description   |
|---------------|---|---|
| Input AC Good | Front panel – LED2 (see figure 6)           | ON state LED indicates operation within specified input voltage range. GREEN LED indicator goes to an OFF condition on PFAIL signal failure state.  |
| Fan Good      | Front panel – LED1 (see figure 6)           | ON state LED indicates normal fan operation and no OTP status. GREEN LED indicator goes to an OFF condition on FAN_FAIL / OTP signal failure state. |
| Output Good   | Module connector / rear side (see figure 7) | GREEN LED indicates that module output voltage is over minimum regulation limit. GREEN LED indicator goes to RED on PG_HI signal failure state.     |

### 8. Connector Details

### 8.1 Input Connector Information

LPM409 front panel connectors pinout refers to Figure 4 and chapter 8.2 and 8.3.

Note: See chapter 10.1 Mating Connections and cables for Input cable information.



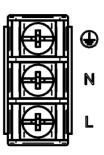


Figure 4. LPM409 Front Panel Connectors Pinout View

## 8.2 Input Connector - Power Pinout

| Signal<br>Name | Pin#     | Туре                 | Recommended wires         | V max<br>I max |
|----------------|----------|----------------------|---------------------------|----------------|
| Earth          | <b>(</b> | Earth / Chassis      | Min. 0,75 mm <sup>2</sup> |                |
| AC Neutral     | N        | Input Power AC       | Max. torque on screws:    | 264 Vrms       |
| AC Line        | L        | Input Power AC Fused | IIINI G,1                 | 10 Arms        |

### 8.3 Signal Connector - Pinout

| Signal<br>Name | Pin<br># | Wire Color | Туре                            | Signal reference | Low level<br>High level    | V max<br>I max |
|----------------|----------|------------|---------------------------------|------------------|----------------------------|----------------|
| +5V            | 1        | Red        | Aux Output                      | RTN_D            | -                          | 5.0Vdc         |
| +5V            | 2        | Red        | Aux Output                      | RTN_D            | -                          | 1.0Adc         |
| RTN_D          | 3        | Black      | Logic Reference Potential       | -                | -                          | -              |
| RTN_D          | 4        | Black      | Logic Reference Potential       | -                | -                          | -              |
| PFAIL          | 5        | Yellow     | Open Collector Output + ZD 5.6V | RTN_D            | <400mV@<br>20mA<br>Pull up | -<br>20mA      |
| FAN_FAIL       | 6        | Green      | Open Collector Output + ZD 5.6V | RTN_D            | <400mV@<br>20mA<br>Pull up | -<br>20mA      |

## 8.4 Output Connector Information

The LPM409 output C connector (refer to *Figures 5 and 7*) provides signal information across its 8-pin output. Refer to *chapter 8.6* for pinout details.

Output Signal connector type: JST-S08B-PUDSS-1

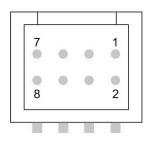


Figure 5. LPM409 Module Output C Signal Connector Pinout

## 8.5 Module Outputs A, B Bus-Bar Pinout

| Signal Name | Pin#  | Туре            | Signal reference | Low level<br>High level | V max<br>I max                  |
|-------------|-------|-----------------|------------------|-------------------------|---------------------------------|
| Vout+       | Vout+ | Output Power DC | Vout-            | -                       | See Module<br>Selection Table 1 |
| Vout-       | Vout- | Output Power DC | -                | -                       | See Module<br>Selection Table 1 |

## 8.6 Module Output C Signal Connector Pinout

| Signal Name | Pin# | Туре   | Signal reference | Low level<br>High level | V max<br>I max |
|-------------|------|--|------------------|-------------------------|----------------|
| VS+         | 1    | Output voltage sense wire.<br>Internally connected to Vout+ via<br>51 Ω. | Vout+            | -                       | 0.5V<br>10mA   |
| PG_LO       | 2    | Reference for PG_HI internally connected to signal ground.               | -                | •                       | -<br>10mA      |
| VS-         | 3    | Output voltage sense wire.<br>Internally connected to Vout- via<br>51 Ω. | Vout-            | -                       | 0.5V<br>10mA   |
| CS          | 4    | Active Current Share pin.  | PG_LO/EN_LO      | -                       | 5V<br>-        |
| PG_HI       | 5    | Open collector output, protected by 5.1V zener diode.                    | PG_LO/EN_LO      | <400mV@5mA<br>Pull up   | -<br>5mA       |
| EN_HI       | 6    | Input internally pulled up and protected by 5.1V zener diode.            | PG_LO/EN_LO      | <400mV@2mA<br>Open      | -<br>2mA       |
| EN_LO       | 7    | Reference for EN_HI internally connected to signal ground.               | -                | -                       | -<br>10mA      |
| N/C         | 8    | -  | -                | -                       | -              |

## 9. Mechanical Drawings

• **Overall Dimensions:** 11.9" x 3.6" x 1.6" (303.7 x 92 x 40.64mm)

• Weight: 1.38 kg (including 4 modules)

All drawing dimensions are shown in millimeters, unless otherwise notated.

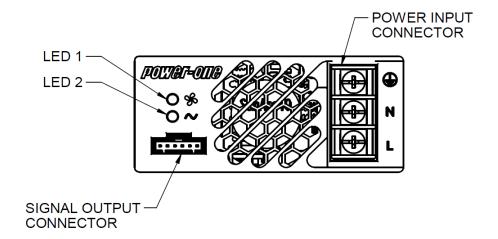


Figure 6. Front View

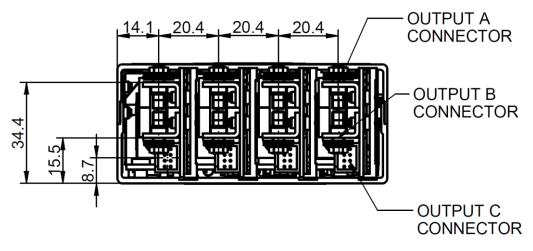


Figure 7. Rear View



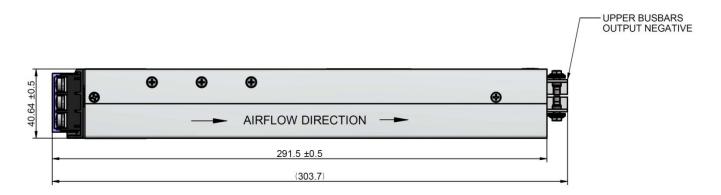


Figure 8. Side View

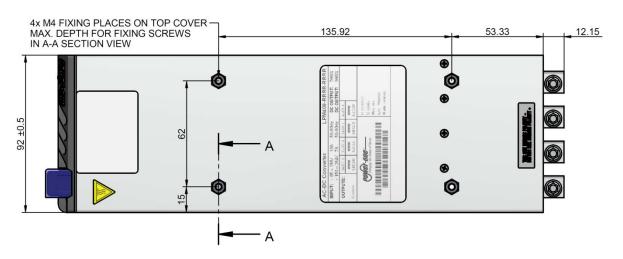


Figure 9. Top View

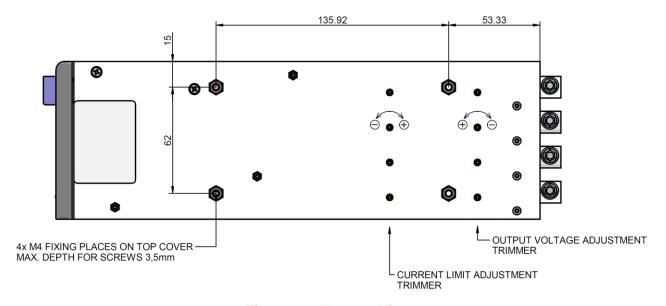


Figure 10. Bottom View



### 10. Accessories

### **10.1 Mating Connections and Cables**

All the power and signal cables and mating connectors are not included in the LPM409 standard package. These all needs to be extra ordered.

Front panel Signal cable: Power-One accessory LPM000-LEAD-03, see *figure 11*.
 Output Signal cable: Power-One accessory LPM000-LEAD-02, see *figure 12*.

• Output signal mating connector: JST-PUDP-08-S, JST-SPUD-001T-P0.5

• Output Power Cable: 14AWG – 10AWG depend on the output current, min. 85°C thermal class

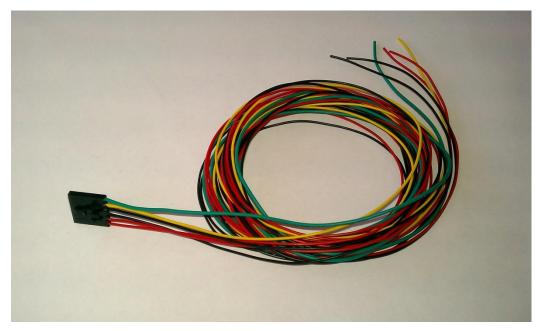
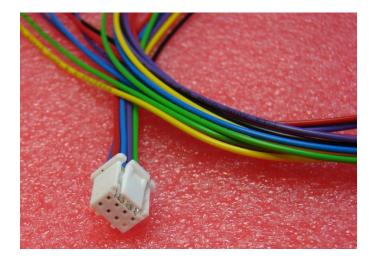
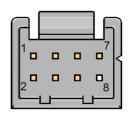


Figure 11. LPM000-LEAD-03 Output Leads





## Figure 12. LPM000-LEAD-02 Output Leads

10.2 Signal Output wire colors

| Pin # | Signal Name | Wire Color |
|-------|-------------|------------|
| 1.    | VS+         | Red        |
| 2.    | PG_LO       | Violet     |
| 3.    | VS-         | Blue       |
| 4.    | CS          | Yellow     |
| 5.    | PG_HI       | Violet     |
| 6.    | EN_HI       | Black      |
| 7.    | EN_LO       | Green      |
| 8.    | N/C         | -          |

10.3 Paralleling Busbars

| Item           | Description   | Model |  |
|----------------|---|-------|--|
| LPM000-BBAR-01 | Busbar with 1 opening for module connection and one for cable connection screw M6x8mm, max. 250A. |       |  |
| LPM000-BBAR-02 | Busbar for parallel connection of two modules.  |       |  |
| LPM000-BBAR-03 | Busbar for parallel connection of three modules.  |       |  |
| LPM000-BBAR-04 | Busbar for parallel connection of four modules.   |       |  |

Note: by using busbars (paralleling modules) you will need also the signal output leads LPM000-LEAD-02, one for each module connected by the same busbar. The interconnection is shown in Fig.16 (especially all pins 4 - CS and all pins 2,7 – SGND must be reconnected).

The example of LPM409-AAAA-DXXX assembly configuration is on the *Figure 13*. Note that for 180A output current this is the most preferred arrangement to achieve best thermal flow. Connect two power cables for each pole (2x35mm²). Use 105°C thermal class isolation.

Another example of configuration LPM409-BBCC-DXXX is shown on Figure 15.



Figure 13. LPM409-AAAA-DXXX parallel configuration



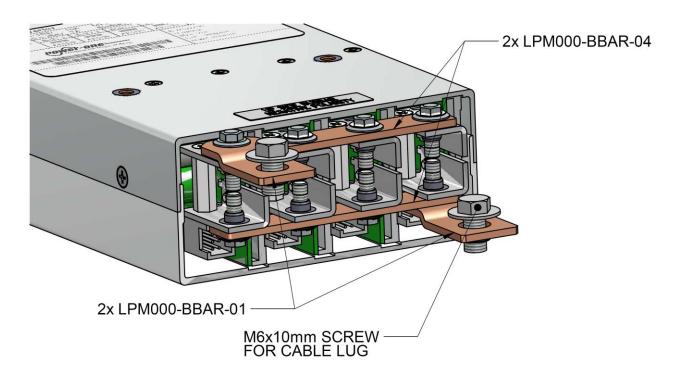


Figure 14. LPM409-AAAA-DXXX Output Busbars arrangement

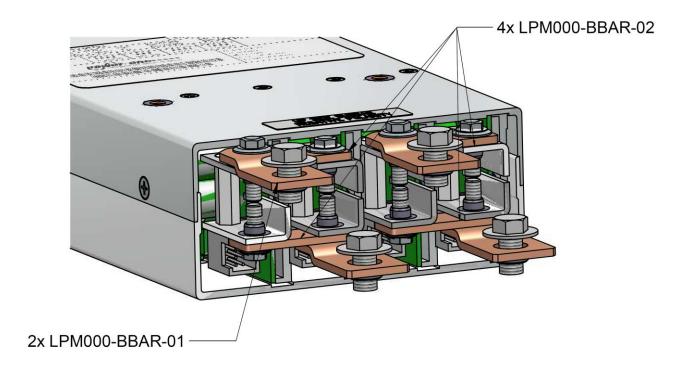


Figure 15. LPM409-BBCC-DXXX Output Busbars arrangement



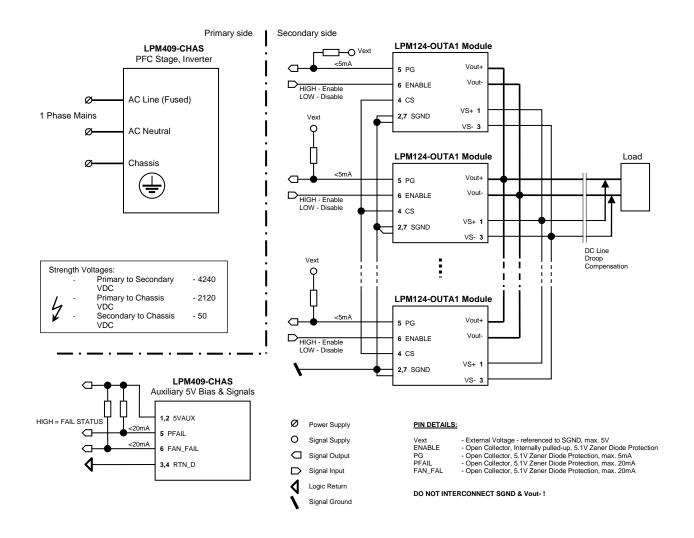


Figure 16. LPM409 - Connection Diagram

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