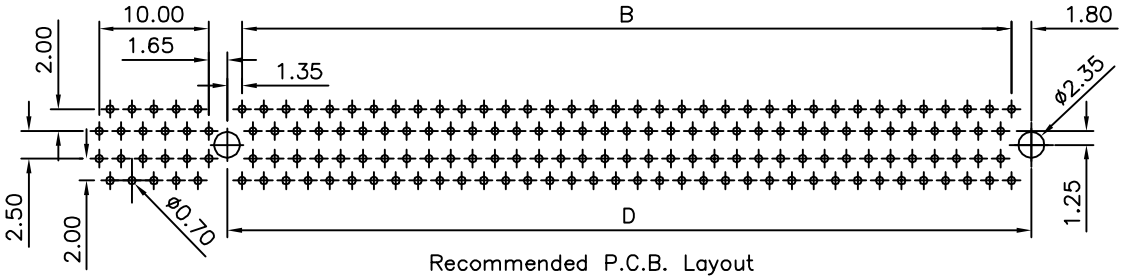
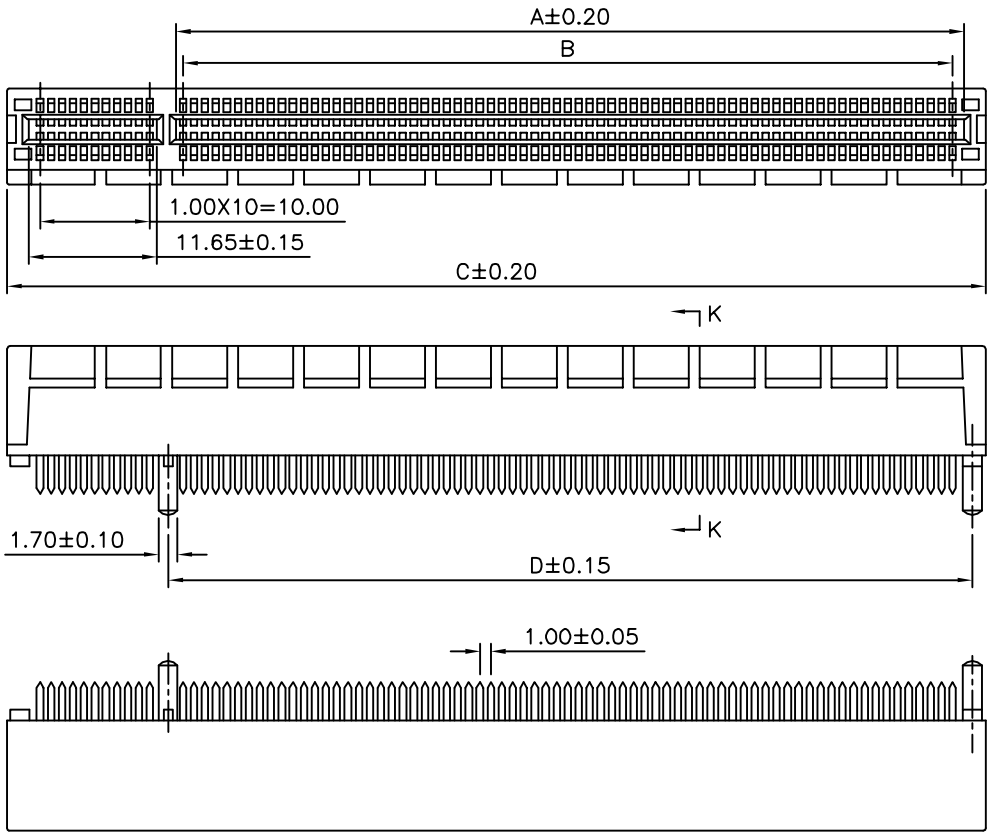
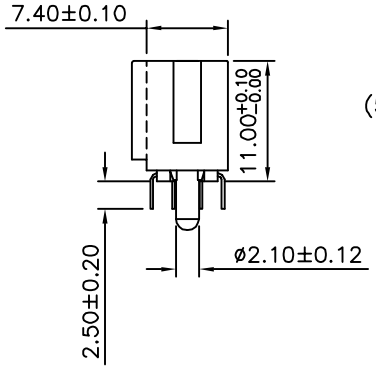
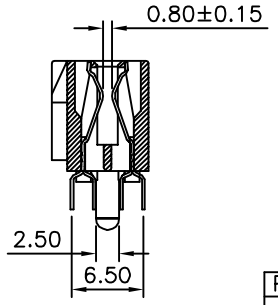
File No.	080EI05233	SHEET	1/1



Recommended P.C.B. Layout







Section K–K

⚠1 SPECIFICATION

Withstand Voltage:500V AC For 1 Minute
Contact Resistance:30 Milliohms Max.
Insulation Resistance:1000 Megaohms Min.
Operation Temperature Range:−55°C~+85°C
Contact Insertion Force:0.115Kg Max. Per Contact Pair
Contac Withdrawal Force:0.015Kg Min. Per Contact Pair
Terminal Retention Force(In Housing):0.5Kg Min. Per Terminal
Housing:PA66+G.F.,Black
⚠2 Contact:Brass
RoHS Compliant

⚠2 PCIE (1) XXX (2) D (3) − SXX (4) − H (5)

(1) PCIE:1.0 PCI Express Edge Slot
(2) XXX:No. Of Position
(3) D:DIP Straight
(4) SXX:Contact Finish
Blank:Gold Flash On Contact Area
Tin Plated On Solder Tails,Over Nickel
S15:Gold Plated 15u" On Contact Area
Tin Plated On Solder Tails,Over Nickel
S30:Gold Plated 30u" On Contact Area
Tin Plated On Solder Tails,Over Nickel
(5) H:RoHS Compliant

Pin	A	B	C	D
36	7.65	6.00	25.00	9.15
64	21.65	20.00	39.00	23.15
98	38.65	37.00	56.00	40.15
164	71.65	70.00	89.00	73.15

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AUK CONTRACTORS CO., LTD.

CATEGORY		APPROVED BY	MEI
PART No. ⚠2	PCIEXXXD−SXX−H	CHECKED BY	HZF
DRAWING No.	PCIEXXXD−SXX−H	DRAWING BY	WSS
SCALE	NONE	REV.	A2

UNLESS OTHERWISE SPECIFIED TOLERANCES ARE
.X ±0.20
.XX ±0.10
.XXX ±0.001
ANG. ±3°
UNIT mm

NO.	REV.	REVISIONS	CHK	DATE
⚠2	A2	Modify P/N:PCIEXXXD−H−−> PCIEXXXD−SXX−H	HZF	02/22/2012
⚠1	A1	Modify Spec.	HZF	05/20/2011
⚠0	A0	NEW RELEASE	HZF	07/07/2008