# **TFT COLOR LCD MODULE**

# NL10276BC24-19D

31cm (12.1 Type) XGA LVDS interface (1port)



DOD-PP-0800 (2nd edition)

This DATA SHEET is updated document from DOD-PP-0461(1).

All information is subject to change without notice. Please confirm the sales representative before starting to design your system.

### **INTRODUCTION**

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The **Specific** quality grade applies to the products developed, designed and manufactured in accordance with the standards or quality assurance program designated by a customer who requires an extremely higher level of reliability and quality for such products.

Examples: Military systems, aircraft control equipment, aerospace equipment, nuclear reactor control systems, medical equipment/devices/systems for life support, etc.

The quality grade of this product is the "Standard" unless otherwise specified in this document.

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### **1. OUTLINE**

### **1.1 STRUCTURE AND PRINCIPLE**

Color LCD module NL10276BC24-19D is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing circuit, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

### 1.2 APPLICATION

• FCNOTE

### **1.3 FEATURES**

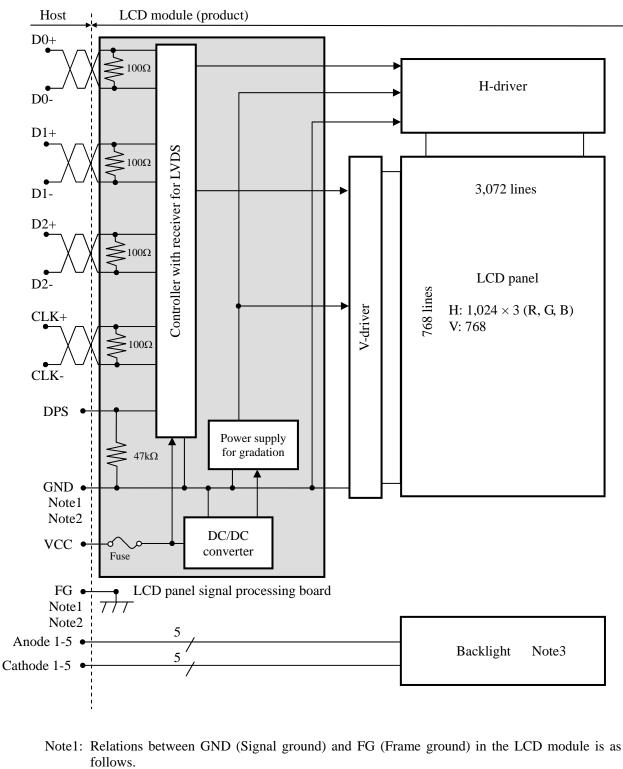
- High luminance
- High contrast
- Wide viewing angle
- LVDS interface
- Reversible-scan direction
- LED backlight type
- Acquisition product for UL60950-1/CSA-C22.2 No.60950-1-03 (File number: E170632)
- Compliance with the European RoHS directive (2002/95/EC)

## 2. GENERAL SPECIFICATIONS

Display area	245.76 (H) × 184.32 (V) mm						
Diagonal size of display	31cm (12.1 inches)						
Drive system	a-Si TFT active matrix						
Display color	262,144 colors						
Pixel	1,024 (H) × 768 (V) pixels						
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe						
Dot pitch	$0.08 (H) \times 0.24 (V) mm$						
Pixel pitch	$0.24 (H) \times 0.24 (V) mm$						
Module size	260.0 (W) × 200.0 (H) × 6.9 (D) mm (typ.)						
Weight	305 g (typ.)						
Contrast ratio	600:1 (typ.)						
Viewing angle	<ul> <li>At the contrast ratio ≥ 10:1</li> <li>• Horizontal: Right side 70° (typ.), Left side 70° (typ.)</li> <li>• Vertical: Up side 60° (typ.), Down side 60° (typ.)</li> </ul>						
Designed viewing direction	<ul> <li>At DPS= Low or Open: Normal scan</li> <li>Viewing direction without image reversal: Up side (12 o'clock)</li> <li>Viewing direction with contrast peak: Down side (6 o'clock)</li> <li>Viewing angle with optimum grayscale (γ=2.2): Normal axis (perpendicular)</li> </ul>						
Polarizer surface	Antiglare						
Polarizer pencil-hardness	3H (min.) [by JIS K5400]						
Color gamut	At LCD panel center 40 % (typ.) [against NTSC color space]						
Response time	$Ton + Toff (10\% \leftrightarrow 90\%)$ 25 ms (typ.)						
Luminance	At $IL=20mA / One \ circuit$ 650 cd/m <sup>2</sup> (typ.)						
Signal system	LVDS 1port (Receiver: THC63LVDF84B, THine Electronics Inc. or equivalent) 6bit digital signals for data of RGB colors, Dot clock (CLK), Data enable (DE)						
Power supply voltage	LCD panel signal processing board: 3.3V						
Backlight	LED backlight type						
Power consumption	At IL= 20mA / One circuit, Checkered flag pattern 4.3 W (typ.)						

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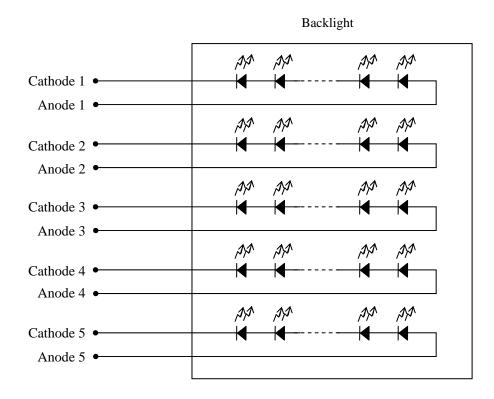
### **3. BLOCK DIAGRAM**



GND-FG	Not connected

Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that GND and FG are connected together in customer equipment.

### Note3: Detail of backlight



### 4. DETAILED SPECIFICATIONS

### 4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification		Unit
Module size	$260.0 \pm 0.3 \text{ (W)} \times 200.0 \pm 0.3 \text{ (H)} \times 6.9 \pm 0.5 \text{ (D)}$	Note1	mm
Display area	245.76 (H) × 184.32 (V)	Note1	mm
Weight	305 (typ.), 320 (max.)		g

Note1: See "8. OUTLINE DRAWINGS".

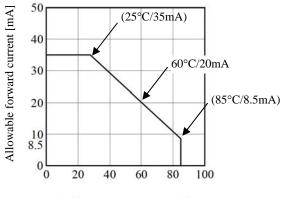
### 4.2 ABSOLUTE MAXIMUM RATINGS

	Parameter	Symbol	Rating	Unit	Remarks
Power supply voltage	LCD panel signal processing board	VCC	-0.3 to +4.0	V	
Input voltage for	Display signals Note1	VD	-0.3 to VCC+0.3	v	-
signals	Function signal Note2	VF	-0.3 10 VCC+0.3	v	
Backlight	Forward current	IL	Note3	mA	per one circuit
Sto	rage temperature	Tst	-20 to +60	°C	-
Oper	rating temperature	Тор	-20 to +60	°C	Note4
			≤ 95	%	$Ta \le 40^{\circ}C$
Re	elative humidity Note5	RH	≤ 85	%	$40^{\circ}\mathrm{C} < \mathrm{Ta} \le 50^{\circ}\mathrm{C}$
			≤ 55	%	$50^{\circ}\mathrm{C} < \mathrm{Ta} \le 60^{\circ}\mathrm{C}$
At	osolute humidity Note5	AH	≤71 Note6	g/m <sup>3</sup>	Ta > 60°C

Note1: D0+/-, D1+/-, D2+/-, CLK+/-

Note2: DPS

Note3: Forward current



Ambient temperature Ta [°C]

Note4: Measured at center of LCD panel surface (including self-heat)

Note5: No condensation

Note6: Water amount at Ta=  $60^{\circ}$ C and RH= 55%

### 4.3 ELECTRICAL CHARACTERISTICS

### 4.3.1 LCD panel signal processing board

	55 <b>111</b> 5 0 0 0						(Ta = 25°C)
Parameter		Symbol	min.	typ.	max.	Unit	Remarks
Power supply voltage		VCC	3.0	3.3	3.6	V	-
Power supply current	ICC	-	420 Note1	680 Note2	mA	at VCC = 3.3V	
Permissible ripple voltage	VRP	-	-	100	mVp-p	for VCC	
Differential input threshold	High	VTH	-	-	+100	mV	at VCM=1.2V
voltage	Low	VTL	-100	-	-	mV	Note3
Terminating resistance		RT	-	100	-	Ω	-
Input voltage for DPS signal	High	VFH	0.7VCC	-	VCC	V	CMOS level
	Low	VFL	0	-	0.3VCC	V	CIVIOS level

Note1: Checkered flag pattern [by EIAJ ED-2522]

Note2: Pattern for maximum current

Note3: Common mode voltage for LVDS receiver

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### 4.3.2 Backlight

					(	Ta= 25°C, Note1, Note2)	_
Parameter	Symbol	min.	typ.	max.	Unit	Remarks	
Forward Current	IL	-	20	35	mA	Note3	
Forward Voltage	VL	-	28.8	31.5	V	at IL= 20mA / One circuit	

Note1: Please drive with constant current.

 Note2: The Luminance uniformity may be changed depending on the current variation between 5 circuits. It is recommended that the current value difference between each circuit is less than 5%.
 Note3: See "4.2 ABSOLUTE MAXIMUM RATINGS Note3

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### 4.3.3 Power supply voltage ripple

This product works, even if the ripple voltage levels are beyond the permissible values as following the table, but there might be noise on the display image.

Power sup	ply voltage	Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3 V	≤ 100	mVp-p

Note1: The permissible ripple voltage includes spike noise.

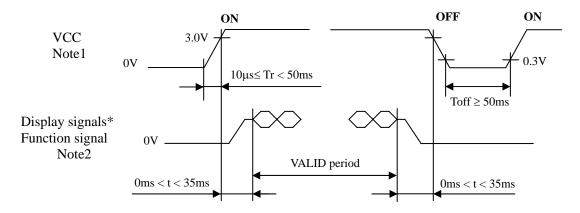
#### 4.3.4 Fuse

Deremeter		Fuse	Rating	Fusing current	Domorka	
Parameter Type		Supplier	Katilig	Fusing current	Remarks	
VCC FCC16202AB		KAMAYA ELECTRIC	2.0A	4.0A	Note1	
vee	FCC10202AB	Co., Ltd.	32V	4.0A	Note1	

Note1: The power supply capacity should be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.

### 4.4 POWER SUPPLY VOLTAGE SEQUENCE

4.4.1 LCD panel signal processing board



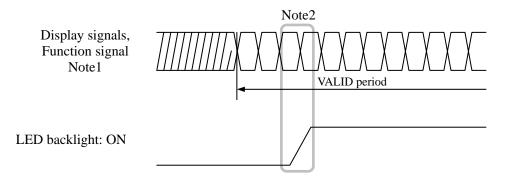
\* These signals should be measured at the terminal of  $100\Omega$  resistance.

- Note1: In terms of voltage variation (voltage drop) while VCC rising edge is below 3.0V, a protection circuit may work, and then this product may not work.
- Note2: Display signals (D0+/-, D1+/-, D2+/-, CLK+/-) and function signal (DPS) must be Low or High-impedance, exclude the VALID period (See above sequence diagram), in order to avoid that internal circuits is damaged.

If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. If customer stops the display and function signals, they should be cut VCC.

Note3: The backlight should be turned on within the valid period of display and function signals, in order to avoid unstable data display.

### 4.4.2 Backlight lighting circuit



- Note1: These are the display and function signals for LCD panel signal processing board.
- Note2: The backlight should be turned on within the valid period of display and function signals, in order to avoid unstable data display.

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### 4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

CN1 socket (LCD module side): FI-XB30SL-HF10 (Japan Aviation Electronics Industry Limited (JAE)) Adaptable plug: FI-X30\* (Japan Aviation Electronics Industry Limited (JAE))

Pin No.	Symbol	Signal	Remarks						
1	VCC	Douvon cumulu	Notal						
2	VCC	Power supply	Note1						
3	GND	Ground	N - 1						
4	GND	Ground	Note1						
5	D0-	- Pixel data	Note2						
6	D0+		Note2						
7	GND	Ground	Note1						
8	D1-	- Pixel data	Note2						
9	D1+		Note2						
10	GND	Ground	Note1						
11	D2-		N / 2						
12	D2+	Pixel data	Note2						
13	GND	Ground	Note1						
14	CLK-		N. 4 2						
15	CLK+	Pixel clock	Note2						
16	GND		Nota1						
17	GND	Ground	Note1						
18	DPS	Selection of scan direction	High:Reverse scanLow or Open:Normal scanNote3						
19	GND	Ground	Note1						
20	GND	Ground	Note1						
21	K1	Cathode 1	-						
22	A1	Anode 1	-						
23	K2	Cathode 2	-						
24	A2	Anode 2	-						
25	К3	Cathode 3	-						
26	A3	Anode 3	-						
27	K4	Cathode 4	-						
28	A4	Anode 4	-						
29	K5	Cathode 5	-						
30	A5	Anode 5	-						

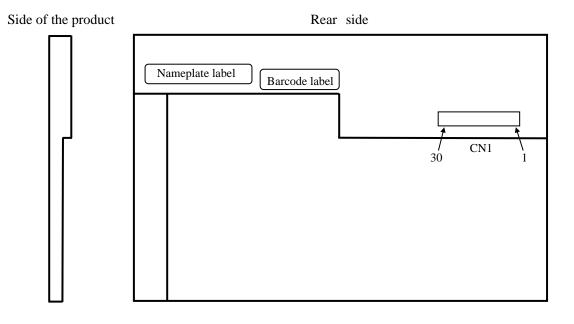
Note1: All GND and VCC terminals should be used without any non-connected lines

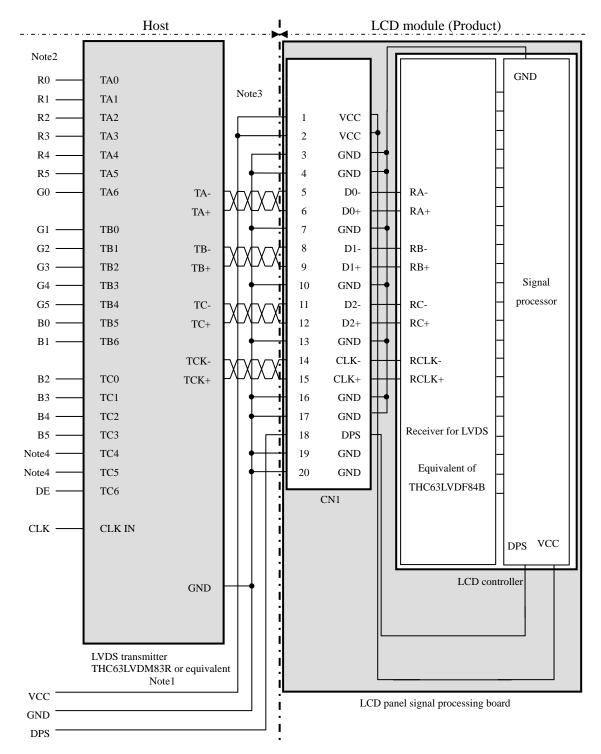
Note2: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note3: See "4.11 SCANNING DIRECTIONS".

Note4: See "4.7 Connection between receiver and transmitter for LVDS".

## 4.6 POSITIONS OF PLUG AND SOCKET

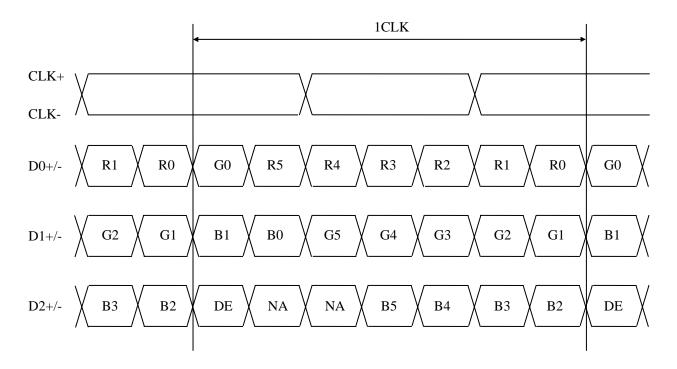




### 4.7 CONNECTION BETWEEN RECEIVER AND TRANSMITTER FOR LVDS

- Note1: Recommended transmitter THC63LVDM83R (THine Electronics Inc.) or equivalent.
- Note2: LSB (Least Significant Bit) R0, G0, B0 MSB (Most Significant Bit) R5, G5, B5
- Note3: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.
- Note4: Input signals to TC4 and TC5 are not used inside the product, but do not keep TC4 and TC5 open to avoid noise problem.

### 4.8 INPUT DATA MAPPING



NA: Not available

## 4.9 DISPLAY COLORS AND INPUT DATA SIGNALS

This product can display equivalent of 262,144 colors in 64 gray scales. Also the relation between display colors and input data signals is as the following table.

Diet	olay colors	Data signal (0: Low level, 1: High level)																	
Dist	Jiay colors	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G4	G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	B 0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
ors	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Basic colors	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
Isic	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
$\mathbf{B}a$	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	1			:							:								
l gr	$\downarrow$			:							:						:		
Red	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
sce	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
ray	<b>↑</b>			:							:						:		
Green gray scale	$\downarrow$			:							:						:		
iree	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
$\cup$		0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
le		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
gray scale	↑			:							:						:		
e g	$\downarrow$			:							:						:		
Blue	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

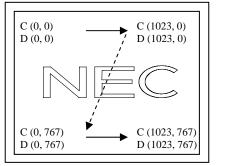
### 4.10 DISPLAY POSITIONS

The following table is the coordinates per pixel (See "4.11 SCANNING DIRECTIONS".).

C (0 R G						
$\begin{pmatrix} C(0, 0) \end{pmatrix}$	C(1, 0)	• • •	C( X, 0)	• • •	C(1022, 0)	C(1023, 0)
C(0, 1)	C(1, 1)	• • •	C( X, 1)	• • •	C(1022, 1)	C(1023, 1)
•	•	•	•	•	•	•
•	•	• • •	•	• • •	•	• • •
•	•	•	•	•	•	•
C( 0, Y)	C( 1, Y)	• • •	C( X, Y)	• • •	C(1022, Y)	C(1023, Y)
•	•	•	•	•	•	•
•	•	• • •	•	• • •	•	•
•	•	•	•	•	•	•
C( 0, 766)	C(1,766)	• • •	C( X, 766)	• • •	C(1022, 766)	C(1023, 766)
C( 0, 767)	C(1,767)	• • •	C( X, 767)	• • •	C(1022, 767)	C(1023, 767)

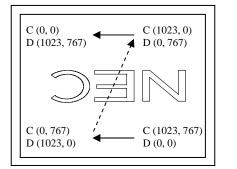
### **4.11 SCANNING DIRECTIONS**

The following figures are seen from a front view. Also the arrow shows the direction of scan.



Note1

Figure1. Normal scan (DPS: Low or Open)



Note1

Figure2. Reverse scan (DPS: High)

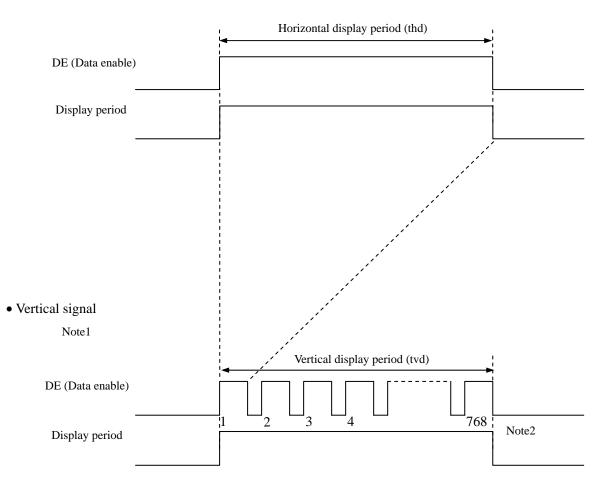
Note1: Meaning of C (X, Y) and D (X, Y)

C (X, Y): The coordinates of the display position (See "**4.10 DISPLAY POSITIONS**".) D (X, Y): The data number of input signal for LCD panel signal processing board

### 4.12 INPUT SIGNAL TIMINGS

- 4.12.1 Outline of input signal timings
  - Horizontal signal

Note1



Note1: This diagram indicates virtual signal for set up to timing. Note2: See "**4.12.3 Input signal timing chart**" for numeration of pulse.

### 4.12.2 Timing characteristics

							(100	ote1, Note2, Note3)
Parameter			Symbol	min.	typ.	max.	Unit	Remarks
	Frequency		1/tc	60.0	65.0	68.0	MHz	15.385 ns (typ.)
CLK		Duty	-				-	
	Rise tin	ne, Fall time	-	-			ns	-
	CLK-DATA	Setup time	-				ns	
DATA	CLK-DAIA	Hold time	-	-			ns	-
	Rise time, Fall time		-		ns			
	Horizontal	Cycle	th	19.67	20.676	22.4	μs	48.363 kHz (typ.)
				-	1,344	-	CLK	
		Display period	thd		1,024		CLK	-
	Vertical (One frame)	Cycle tv	ty	13.3	16.666	18.5	ms	60.0 Hz (typ.)
DE			ιv	780	806	-	Н	00.0 Hz (typ.)
		Display period	tvd		768		Н	-
	CLK-DE	Setup time	-	_			ns	
		Hold time	Hold time -				ns	-
	Rise time, Fall time		-				ns	

Note1: Definition of parameters is as follows.

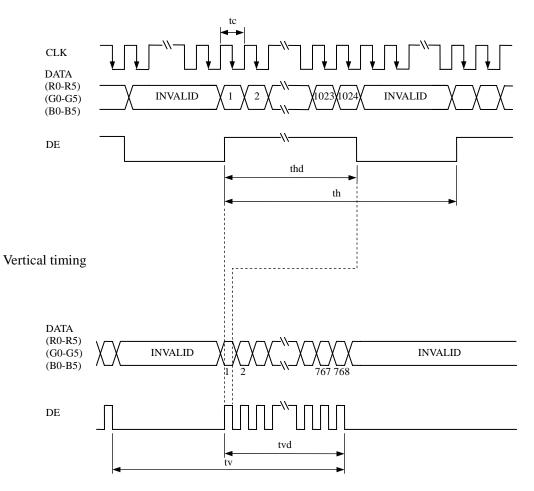
tc = 1CLK, th = 1H

Note2: See the data sheet of LVDS transmitter.

Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).

### 4.12.3 Input signal timing chart

# Horizontal timing



### 4.13 OPTICS

4.13.1 Optical characteristics

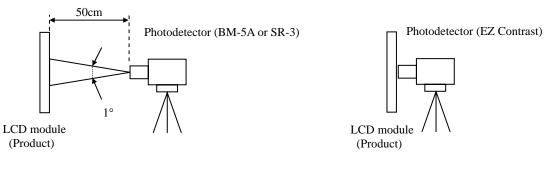
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$									(Note1, 1	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Parameter		Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Luminance			L	450	650	I	cd/m <sup>2</sup>	BM-5A	-
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Contrast rat	tio		CR	300	600	I	-	BM-5A	Note3
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Luminance unif	ormity		LU	-	1.25	1.40	-	BM-5A	Note4
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		White	<b>x</b> coordinate	Wx	0.263	0.313	0.363	-		
$\begin{array}{ c c c c c c c c c } \hline Red & y \ coordinate & Ry & - & 0.346 & - & - & & & & & & & & & & & & & & & $		white	y coordinate	Wy	0.279	0.329	0.379	-		
$\begin{array}{c c c c c c c c c c c c c c c c c c c $		Red	<b>x</b> coordinate	Rx	-	0.564	-	-	-	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Chromaticity		<b>y</b> coordinate	Ry	-	0.346	-	-		
$\frac{\mathbf{y} \text{ coordinate}}{Blue} \begin{array}{c c} \mathbf{y} & \mathbf{y} \text{ coordinate} \\ \hline \mathbf{x} \text{ coordinate} \\ \hline \mathbf{y}  c$	Chromaticity	Green	<b>x</b> coordinate	Gx	-	- 0.348 -	-	SD 3	Note5	
Bluey coordinateBy- $0.134$ -Color gamut $\theta R = 0^{\circ}, \theta L = 0^{\circ}, \theta U = 0^{\circ}, \theta D = 0^{\circ}$ C3540-			y coordinate	Gy	-	0.541	-	-	3K-3	Notes
y coordinateBy- $0.134$ -Color gamut $\theta R = 0^{\circ}, \theta L = 0^{\circ}, \theta U = 0^{\circ}, \theta D = 0^{\circ}$ C3540-		Blue	<b>x</b> coordinate	Bx	-	0.151	-	-		
1 + 1 + 1 + 1 + 1 + 1 + 1 + 1 + 1 + 1 +			<b>y</b> coordinate	By	-	0.134	-	-		
at center, against ivise color space	Color gamut		$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ \theta U = 0^{\circ}, \ \theta D = 0^{\circ}$ at center, against NTSC color space	С	35	40	-	%		
Response time White to Black Ton - 6 15 ms BM-5A	Response time		White to Black	Ton	-	6	15	ms	BM 54	Note6
Black to White Toff - 19 47 ms Black to White No. 19 47 ms			Black to White	Toff	-	19	47	ms	DIVI-JA	Note7
Right $\theta U = 0^{\circ}, \ \theta D = 0^{\circ}, \ CR \ge 10$ $\theta R$ $60$ $70$ $ \circ$	Viewing angle	Right	$\theta U=0^{\circ}, \ \theta D=0^{\circ}, \ CR\geq 10$	θR	60	70	-	0		
Viewing angle Left $\theta U=0^\circ, \theta D=0^\circ, CR \ge 10$ $\theta L$ 60 70 - $\circ$ EZ		Left $\theta U = 0^\circ, \ \theta D = 0^\circ, \ CR \ge 10$	θL	60	70	-	0	EZ	Nota	
Viewing angle Up $\theta R = 0^{\circ}, \theta L = 0^{\circ}, CR \ge 10$ $\theta U$ 50 60 - $\circ$ Contrast		gle		θU	50	60	-	0	Contrast	Note8
Down $\theta R = 0^{\circ}, \theta L = 0^{\circ}, CR \ge 10$ $\theta D$ 50 60 - °		Down	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θD	50	60	-	0		

Note1: These are initial characteristics.

Note2: Measurement conditions are as follows.

Ta= 25°C, VCC= 3.3V, IL= 20mA / One circuit, Display mode: XGA, Horizontal cycle= 1/48.363kHz, Vertical cycle= 1/60.0Hz, DPS= Low or Open: Normal scan

Optical characteristics are measured at luminance saturation after 20minutes from working the product, in the dark room. Also measurement methods are as follows.



Note3: See "4.13.2 Definition of contrast ratio".

Note4: See "4.13.3 Definition of luminance uniformity".

- Note5: These coordinates are found on CIE 1931 chromaticity diagram.
- Note6: Product surface temperature:  $TopF = 27.5^{\circ}C$
- Note7: See "4.13.4 Definition of response times".
- Note8: See "4.13.5 Definition of viewing angles".

### 4.13.2 Definition of contrast ratio

The contrast ratio is calculated by using the following formula.

Contrast ratio (CR) = Luminance of white screen Luminance of black screen

4.13.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

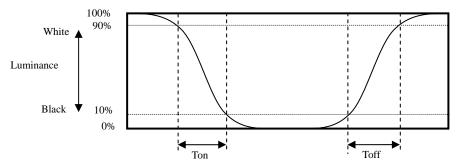
Luminance uniformity (LU) = <u>Maximum luminance from (1) to (5)</u> <u>Minimum luminance from (1) to (5)</u>

The luminance is measured at near the 5 points shown below.

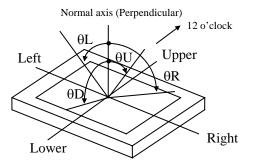
	1	71	5	12	8	353
128		1				2
384				3		
640		4				5

#### 4.13.4 Definition of response times

Response time is measured, the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time it takes the luminance change from 90% down to 10%. Also Toff is the time it takes the luminance change from 10% up to 90% (See the following diagram.).



4.13.5 Definition of viewing angles



### 5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

#### This lifetime is the estimated value, and is not guarantee value.

Condition		Estimated luminance lifetime (Life time expectancy) Note1, Note2, Note3	Unit
LED elementary substance	25°C (Ambient temperature of LED) Continuous operation, IL= 20mA / One circuit	21,000	h

Note1: MTTF is mean time to half-luminance.

Note2: Estimated luminance lifetime is not the value for LCD module but the value for LED elementary substance.

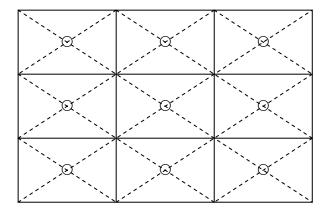
Note3: By ambient temperature, the lifetime changes particularly. Especially, in case the product works under high temperature environment, the lifetime becomes short.

## 6. RELIABILITY TESTS

Test item	Condition	Judgment Note1		
High temperature and humidity (Operation)	<ol> <li>60 ± 2°C, RH = 60%, 240hours</li> <li>Display data is black.</li> </ol>			
Heat cycle (Operation)	<ol> <li>20 ± 3°C1hour 60 ± 3°C1hour</li> <li>50cycles, 4hours/cycle</li> <li>Display data is black.</li> </ol>			
Thermal shock (Non operation)	<ol> <li>20 ± 3°C30minutes 60 ± 3°C30minutes</li> <li>100cycles, 1hour/cycle</li> <li>Temperature transition time is within 5 minutes.</li> </ol>	No display malfunctions		
ESD (Operation)	<ol> <li>150pF, 150Ω, ±10kV</li> <li>9 places on a panel surface Note2</li> <li>10 times each points at 1 sec interval</li> </ol>			
Dust (Operation)	<ol> <li>Sample dust: No. 15 (by JIS-Z8901)</li> <li>15 seconds stir</li> <li>8 times repeat at 1 hour interval</li> </ol>			
Vibration (Non operation)	<ol> <li>5 to 100Hz, 19.6m/s<sup>2</sup></li> <li>1 minute/cycle</li> <li>X, Y, Z directions</li> <li>120 times each directions</li> </ol>	No display malfunctions No physical damages		
Mechanical shock (Non operation)	<ol> <li>539m/ s<sup>2</sup>, 11ms</li> <li>±X, ±Y, ±Z directions</li> <li>5 times each directions</li> </ol>	100 physical daillages		

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.



### 7. PRECAUTIONS

### 7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. **Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS", after understanding these contents!** 

This sign has the meaning that customer will be injured by personnel, if customer has wrong operations.



This sign has the meaning that customer will be injured by personnel or the product will sustain a damage, if customer has wrong operations.

### 7.2 CAUTIONS



\* Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: To be not greater 539m/s<sup>2</sup> and to be not greater 11ms, Pressure: To be not greater 19.6 N (\$\$\phi16mm jig)\$)

7.3 ATTENTIONS

7.3.1 Handling of the product

- ① Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② When the product is put on the table temporarily, display surface must be placed downward.
- ③ When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- (4) The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- (5) Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- <sup>(6)</sup> Do not push nor pull the interface connectors while the product is working.
- ⑦ When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
- ③ Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal for the worst, please wash it out with soap.

☆

### 7.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurring by temperature difference, the product packing box should be opened after enough time being left under the environment of an unpacking room. Evaluate the leaving time sufficiently because a situation of dew condensation occurring is changed by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with packing state)
- ③ Do not operate in high magnetic field. Circuit boards may be broken down by it.
- ④ This product is not designed as radiation hardened.

#### 7.3.3 Characteristics

### The following items are neither defects nor failures.

- ① Response time, luminance and color may be changed by ambient temperature.
- ② Display mura, flicker, vertical seam or small spot may be observed depending on display patterns.
- ③ Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- (4) The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.

#### 7.3.4 Other

- ① All GND and VCC terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ Pack the product with original shipping package, in order to avoid any damages during transportation, when returning the product to NEC for repair and so on.
- (4) The information of China RoHS directive six hazardous substances or elements in this product is as follows.

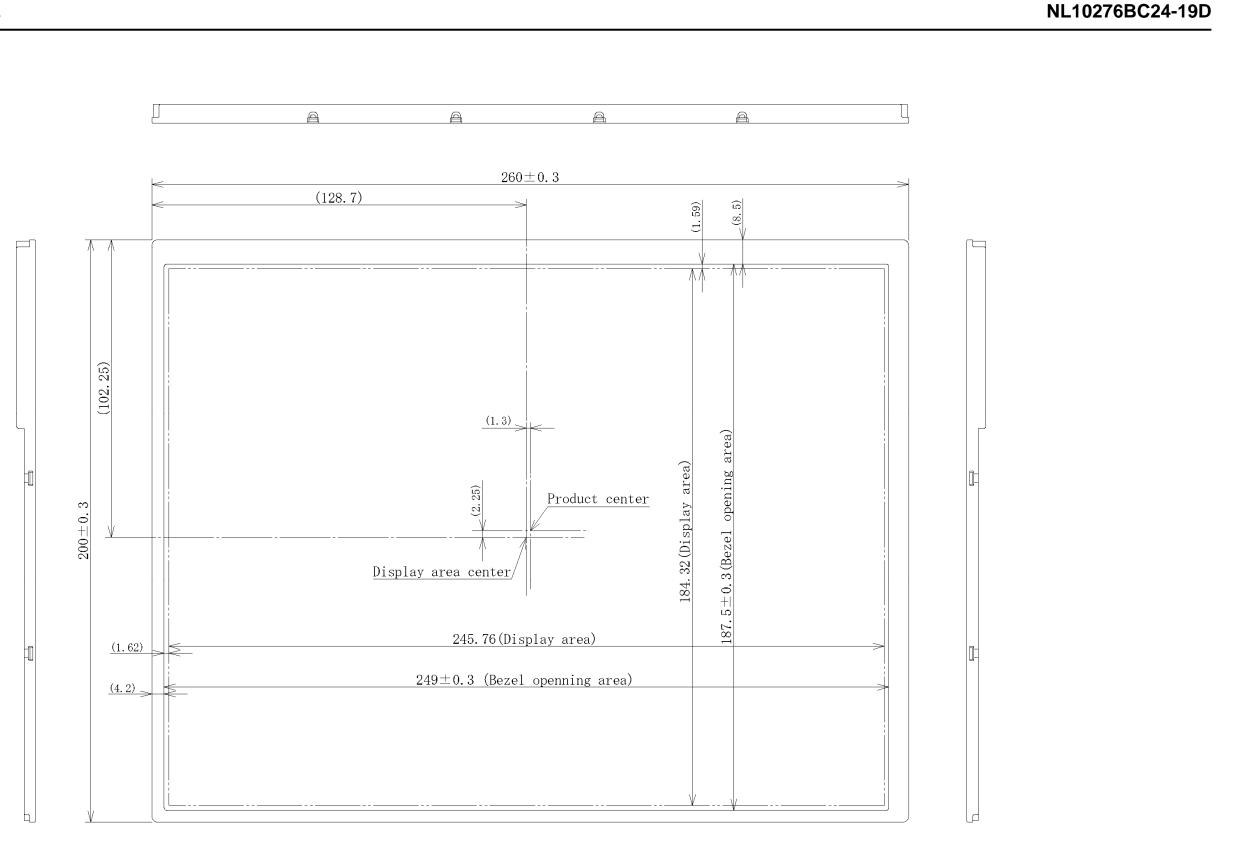
China RoHS directive six l hazardous substances or elements							
Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (Cr VI)	Polybrominated Biphenys (PBB)	Polybrominated Biphenyl Ethers (PBDE)		
×	0	0	0	0	0		

Note1: (): This indicates that the poisonous or harmful material in all the homogeneous materials for this part is equal or below the limitation level of SJ/T11363-2006 standard regulation.

X: This indicates that the poisonous or harmful material in all the homogeneous materials for this part is above the limitation level of SJ/T11363-2006 standard regulation.

### 8. OUTLINE DRAWINGS

8.1 FRONT VIEW

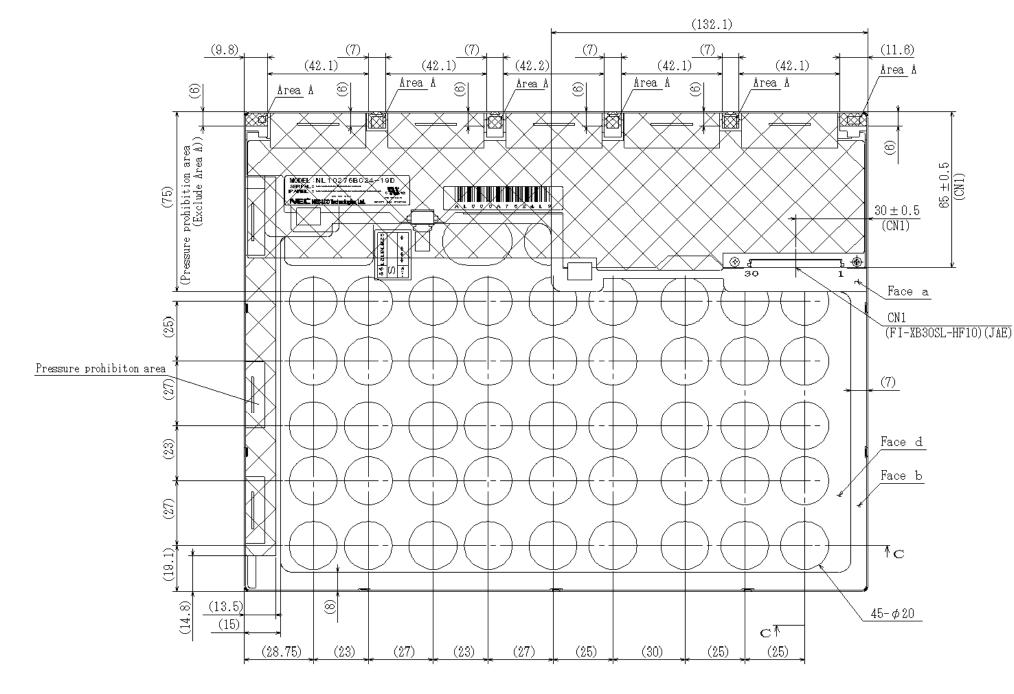


	<b>6</b>	

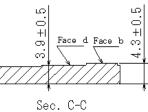
Note1: The values in parentheses are for reference.

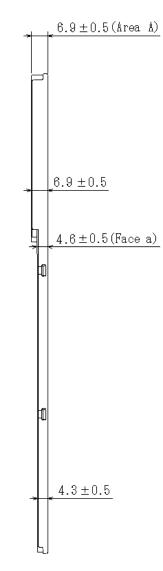
### Unit: mm

8.2 REAR VIEW



Notel: The values in parentheses are for reference.





Unit: mm